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LM140L/LM340L Series 3-Terminal Positive Regulators General Description

The LM140L series of three terminal positive regulators is available with several fixed output voltages making them useful in a wide range of applications. The LM140LA is an improved version of the LM78LXX series with a tighter output voltage tolerance (specified over the full military temperature range), higher ripple rejection, better regulation and lower guiescent current. The LM140LA regulators have ±2% V_{OUT} specification, 0.04%/V line regulation, and 0.01%/mA load regulation. When used as a zener diode/resistor combination replacement, the LM140LA usually results in an effective output impedance improvement of two orders of magnitude, and lower guiescent current. These regulators can provide local on card regulation, eliminating the distribution problems associated with single point regulation. The voltages available allow the LM140LA to be used in logic systems, instrumentation, Hi-Fi, and other solid state electronic equipment. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltages and currents.

The LM140LA/LM340LA are available in the low profile metal three lead TO-39 (H) and the LM340LA are also available in the plastic TO-92 (Z). With adequate heat sinking the regulator can deliver 100 mA output current. Current limiting is included to limit the peak output current to a safe value. Safe area protection for the output transistor is pro-

vided to limit internal power dissipation. If internal power dissipation becomes too high for the heat sinking provided, the thermal shut-down circuit takes over, preventing the IC from overheating.

For applications requiring other voltages, see LM117L Data Sheet.

Features

- Line regulation of 0.04%/V
- Load regulation of 0.01%/mA
- Output voltage tolerances of ±2% at T_j = 25°C and ±4% over the temperature range (LM140LA) ±3% over the temperature range (LM340LA)
- Output current of 100 mA
- Internal thermal overload protection
- Output transistor safe area protection
- Internal short circuit current limit
- Available in metal TO-39 low profile package (LM140LA/LM340LA) and plastic TO-92 (LM340LA)

Output Voltage Options

LM140LA-5.0	5V	LM340LA-5.0	5V
LM140LA-12	12V	LM340LA-12	12V
LM140LA-15	15V	LM340LA-15	15V



LM140L/LM340L Series

Connection Diagrams (Continued)



Bottom View Order Number LM340LAZ-5.0, LM340LAZ-12 or LM340LAZ-15 See NS Package Number Z03A

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications. (Note 5)

35V
Internally Limited
–55°C to +125°C
0°C to +70°C

Maximum Junction Temperature+150°CStorage Temperature RangeMetal Can (H package)-65°C to +150°CMolded TO-92-55°C to +150°CLead Temperature (Soldering, 10 sec.)+300°CPlastic TO-92+230°C

Electrical Characteristics

Test conditions unless otherwise specified. T_A = -55°C to +125°C (LM140LA), T_A = 0°C to +70°C (LM340LA), I_O = 40 mA, C_{IN} = 0.33 μ F, C_O = 0.01 μ F.

Output Voltage Option		5.0V		12V		15V							
Input Voltage (unless otherwise noted)		10V		19V		23V		Units					
Symbol	Parameter		Conditions	Min	Тур	Мах	Min	Тур	Max	Min	Тур	Мах	
Vo	Output Voltage	T _j = 25°C		4.9	5	5.1	11.75	12	12.25	14.7	15	15.3	
	Output Voltage	LM140LA	I _O = 1 – 100 mA	4.8		5.2	11.5		12.5	14.4		15.6	
	Over Temp.			(7.2–20)			(14.5–27)		(17.6–30)		V		
	(Note 4)	LM340LA	$I_{O} = 1 - 100 \text{ mA or}$	4.85		5.15	11.65		12.35	14.55		15.45	
			$I_{O} = 1 - 40$ mA and	((7–20))	(1	4.3–2	27)	(1	7.5–3	0)	
			$V_{IN} = ()V$										
ΔV_{O}	Line Regulation	T _j = 25°C	I _O = 40 mA		18	30		30	65		37	70	
			$V_{IN} = ()V$	((7–25))	(1	4.2–3	30)	(1	7.3–3	0)	
			I _O = 100 mA		18	30		30	65		37	70	mV
			$V_{IN} = ()V$	(7	7.5–25	5)	(1	4.5–3	30)	(1	7.5–3	0)	
	Load	$T_j = 25^{\circ}C$	$I_{O} = 1 - 40 \text{ mA}$		5	20		10	40		12	50	
	Regulation		I _O = 1 – 100 mA		20	40		30	80		35	100	
	Long Term				12			24			30		mV
	Stability												1000 hrs
I _O	Quiescent	T _j = 25°C			3	4.5		3	4.5		3.1	4.5	mA
	Current	T _j = 125°C				4.2			4.2			4.2	
ΔI_Q	Quiescent	T _j = 25°C	Δ Load I _O = 1 – 40 mA			0.1			0.1			0.1	
	Current		ΔLine			0.5			0.5			0.5	mA
	Change		$V_{IN} = ()V$	(7	7.5–25	5)	(1	4.3–3	30)	(1	7.5–3	0)	
V _N	Output Noise	T _j = 25°C (Note 3)		40			80			90		μV
	Voltage	f = 10 Hz-10 kHz											
ΔV_{IN}	Ripple Rejection	f = 120 Hz, V _{IN} = ()V		55	62		47 54		45 52		dB		
ΔV _{OUT}				(7	7.5–18	3)	(1	4.5–2	25)	(17	7.5–28	.5)	
	Input Voltage												
	Required to	T _j = 25°C,	l _o = 40 mA	7			14.2			17.3			V
	Maintain Line												
	Regulation												

Note 1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: Thermal resistance of H-package is typically 26°C/W θ_{jC} , 250°C/W θ_{jA} still air, and 94°C/W θ_{jA} 400 lf/min of air. For the Z-package is 60°C/W θ_{jC} , 232°C/W θ_{jA} still air, and 88°C/W θ_{jA} at 400 lf/min of air. The maximum junction temperature shall not exceed 125°C on electrical parameters.

Note 3: It is recommended that a minimum load capacitor of 0.01 µF be used to limit the high frequency noise bandwidth.

Note 4: The temperature coefficient of V_{OUT} is typically within 0.01% $V_O/^\circ C.$

Note 5: A military RETS specification is available upon request. At the time of printing, the LM140LA-5.0, -12, and -15 RETS specifications complied with the Min and Max limits in this table. The LM140LAH-5.0, LM140LAH-12, and LM140LAH-15 may also be procured as Standard Military Drawings.



Typical Performance Characteristics



00778207















00778208





00778210

Output Impedance





LM140L/LM340L Series

Typical Applications (Continued)



 $\label{eq:VOUT} V_{OUT} = 5V + (5V/R1 + I_0) \ R2 \\ 5V/R1 = 3 \ I_0 \ \text{load} \ \text{regulation} \ (L,) \ [(R1 + R2)/R1] \ (L, \ \text{of} \ LM140LA-5.0)$

Equivalent Circuit





LM140L/LM340L Series

Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 5.2 4.19 EJECTION MARK Ø1.6 MAX ∓0.38 MAX 5.2 SEATING PLANE 2.29 MAX (UNCONTROLLED LEAD DIA) 14.2 0.55 0.40 TYP-0.50 1 27+0 05 - 2.54±0.1 2.27 2.10 DIMENSIONS ARE IN MILLIMETERS m m ZO3A (Rev G) TO-92 Plastic Package (Z) Order Number LM340LAZ-5.0, LM340LAZ-12, or LM340LAZ-15 NS Package Number Z03A National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications. For the most current product information visit us at www.national.com. LIFE SUPPORT POLICY NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein: 1. Life support devices or systems are devices or systems 2. A critical component is any component of a life support which, (a) are intended for surgical implant into the body, or device or system whose failure to perform can be reasonably (b) support or sustain life, and whose failure to perform when expected to cause the failure of the life support device or properly used in accordance with instructions for use system, or to affect its safety or effectiveness. provided in the labeling, can be reasonably expected to result in a significant injury to the user. BANNED SUBSTANCE COMPLIANCE National Semiconductor manufactures products and uses packing materials that meet the provisions of the Customer Products Stewardship Specification (CSP-9-111C2) and the Banned Substances and Materials of Interest Specification (CSP-9-111S2) and contain no "Banned Substances" as defined in CSP-9-111S2.

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