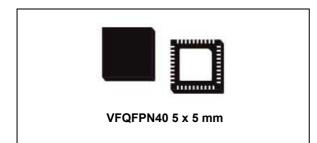


# PM6773

### 3+1 dual channel VR13 digital multiphase controller with PMBus™

Data brief



#### Features

- Intel<sup>®</sup> VR13 3+1 phases compact digital controller
- VR13 compliant w/ 25 MHz SVID bus rev1.7
- High performance digital control loop (digital STVCOT™)
- Proprietary auto tuning technique
- Fully configurable through PMBus™
- AutoDPM automatic dynamic phase management
- Remote sense; 0.5% Vout accuracy with calibration
- Current monitor signal with calibration
- Auto calibration capability for current and voltage sense
- Programmable voltage positioning
- OV, UV and FB disconnection protection
- Embedded non-volatile memory (NVM)
- Black box recorder
- VFQFPN40 5 x 5 mm package
- Universal footprint and pinout for multisourcing

#### Applications

- High current power regulation for Intel VR13
  based microprocessors
- DDR memory power regulation for Intel VR13 based systems

### Description

The PM6773 device is a high performance digital dual controller designed to power Intel's VR13 processors: all required parameters are programmable through the PMBus™ interface.

The device utilizes digital technology to implement all control and power management functions to provide maximum flexibility and performance. The NVM is embedded to store custom configurations.

The PM6773 device features up to 3 +1 phase programmable operation. The PM6773 device supports power state transitions featuring VFDE, and programmable DPM maintaining the best efficiency over all loading conditions without compromising transient response. The device assures fast and independent protection against load overcurrent, under/overvoltage and feedback disconnections.

The device is available in a VFQFPN40 5 x 5 mm package.

#### Table 1. Device summary

Order code	Package	Packing
PM6773	VFQFPN40 5 x 5 mm	Tray
PM6773TR		Tape and reel

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For further information contact your local STMicroelectronics sales office.

## **Revision history**

Date	Revision	Changes
20-Dec-2016	1	Initial release.
08-Feb-2017	2	Updated main title <i>on page 1</i> . Removed the PM6774 and PM6775 devices from the whole document. Updated "VR13 compliant w/ 25 MHz SVID bus rev1.7" in <i>Section : Features on page 1</i> .

Table 2. Document revision history



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