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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HAT1126R, HAT1126RJ

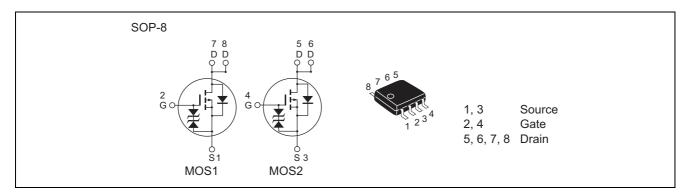
Silicon P Channel Power MOS FET High Speed Power Switching

REJ03G0406-0100 Rev.1.00 Sep.10.2004

Features

- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting
- "J" is for Automotive application High temperature D-S leakage guarantee Avalanche rating

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Cumbal	Ra	l lmit	
	Symbol	HAT1126R	HAT1126RJ	Unit
Drain to source voltage	V _{DSS}	-60	-60	V
Gate to source voltage	V _{GSS}	±20	±20	V
Drain current	I _D	-6.0	-6.0	А
Drain peak current	I _D (pulse) ^{Note1}	-48	-48	А
Avalanche current	I _{AP} Note4	_	-6.0	А
Avalanche energy	E _{AR} Note4	_	3.08	mJ
Channel dissipation	Pch ^{Note2}	2	2	W
Channel dissipation	Pch ^{Note3}	3	3	W
Channel temperature	Tch	150	150	°C
Storage temperature	Tstg	-55 to +150	-55 to +150	°C

Notes: 1. $PW \le 10\mu s$, duty cycle $\le 1\%$

- 2. 1 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s
- 3. 2 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s
- 4. Value at Tch = 25°C, Rg \geq 50 Ω

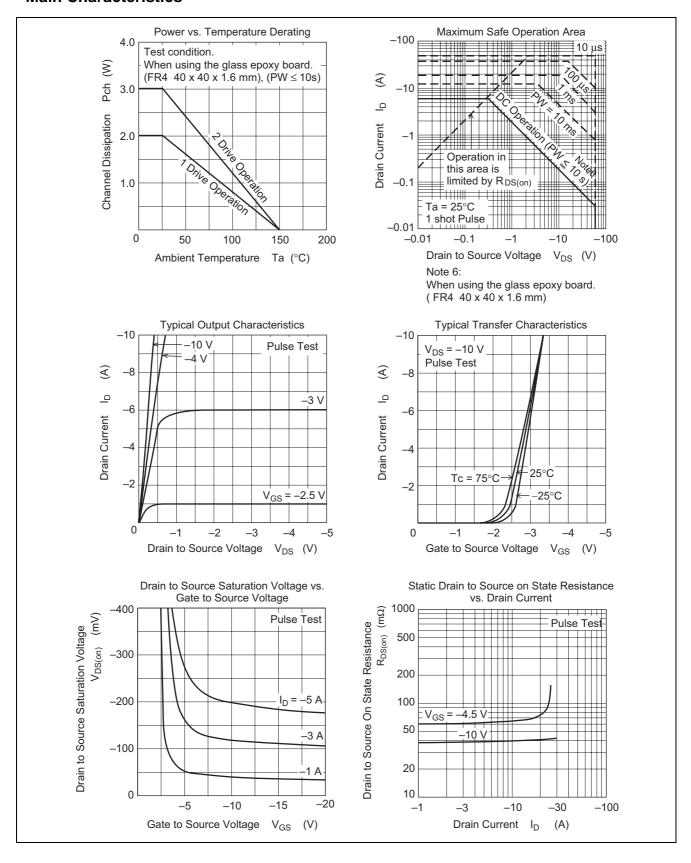
Electrical Characteristics

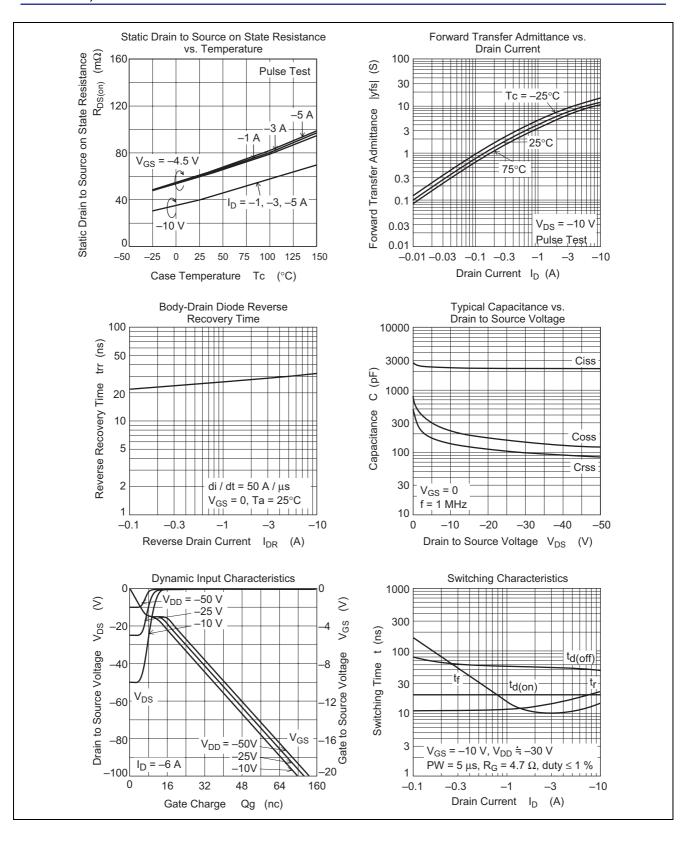
 $(Ta = 25^{\circ}C)$

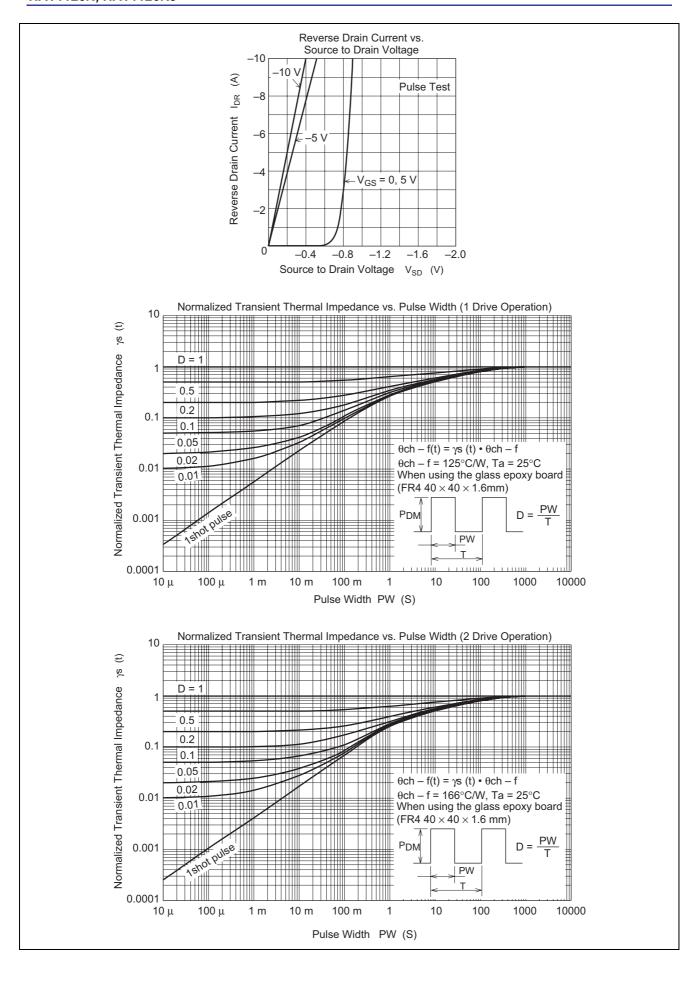
Item		Symbol	Min	Тур	Max	Unit	Unit
Drain to source breakdown voltage		$V_{(BR)DSS}$	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to Source breakdown voltage		$V_{(BR)GSS}$	±20	_	-	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current		I _{DSS}	_	_	-1	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Zero gate voltage	HAT1126R	I _{DSS}	_	_	-	μΑ	$V_{DS} = -48 \text{ V}, V_{GS} = 0$
drain current	HAT1126RJ	I _{DSS}	_	_	-10	μΑ	Ta = 125°C
Gate to source leak current		I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage		$V_{GS(off)}$	-1.0	_	-2.5	V	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$
Forward transfer admittance		y _{fs}	4.0	7.0	-	S	$I_D = -3.0 \text{ A}^{\text{Note5}}, V_{DS} = -10 \text{ V}$
Static drain to source on state		R _{DS(on)}	_	40	50	mΩ	$I_D = -3.0 \text{ A}^{\text{Note5}}, V_{GS} = -10 \text{ V}$
resistance		R _{DS(on)}	_	60	85	mΩ	$I_D = -3.0 \text{ A}^{\text{Note5}}, V_{GS} = -4.5 \text{ V}$
Input capacitance		Ciss	_	2300	1	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0$
Output capacitance		Coss	_	230	1	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	140	1	pF	
Total gate charge		Qg	_	37	1	nC	$V_{DD} = -25 \text{ V}$
Gate to source charge		Qgs	_	6.5	_	nC	$V_{GS} = -10 \text{ V}$
Gate to drain charge		Qgd	_	8	_	nC	$I_D = -6.0 \text{ A}$
Turn-on delay time		td(on)	_	20	1	ns	$V_{GS} = -10 \text{ V}, I_{D} = -3.0 \text{ A}$
Rise time		tr	_	15	1	ns	$V_{DD} \cong -30 \text{ V}$
Turn-off delay time		td(off)	_	55	1	ns	$R_L = 10 \Omega$
Fall time		tf	_	10	_	ns	$R_G = 4.7 \Omega$
Body-drain diode forward voltage		V_{DF}	_	-0.85	-1.1	V	$I_F = -6.0 \text{ A}, V_{GS} = 0^{\text{Note5}}$
Body-drain diode reverse recovery time		trr	_	30	_	ns	$I_F = -6.0 \text{ A}, V_{GS} = 0$ diF/dt = 100 A / μ s

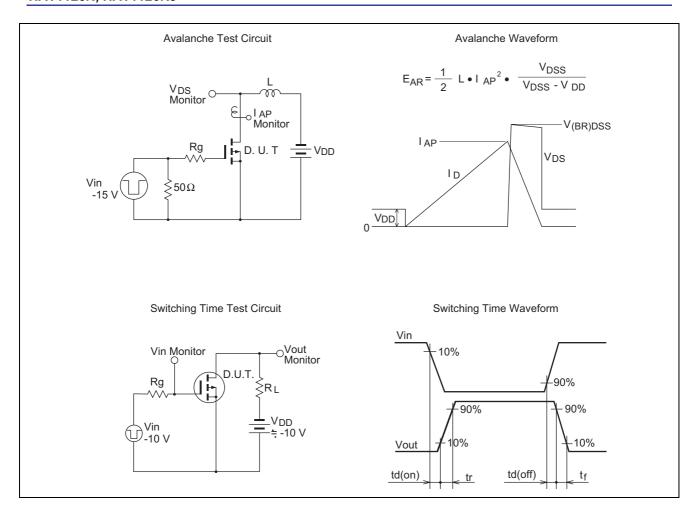
Notes: 5. Pulse test

Main Characteristics

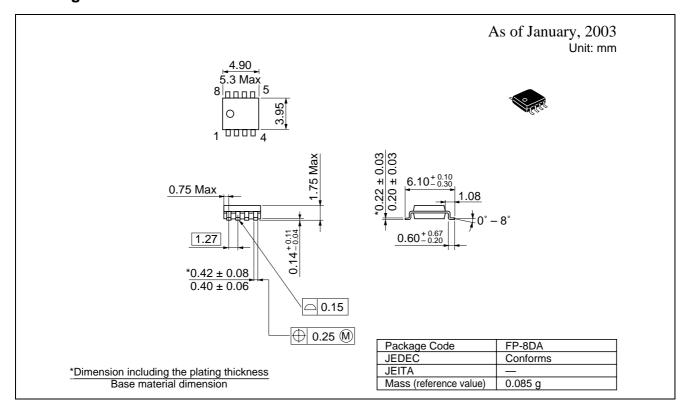








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT1126R-EL-E	2500 pcs	Taping
HAT1126RJ-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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Renesas Technology Singapore Pte. Ltd.
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