Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK2727

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1025-0300

(Previous: ADE-208-526A)

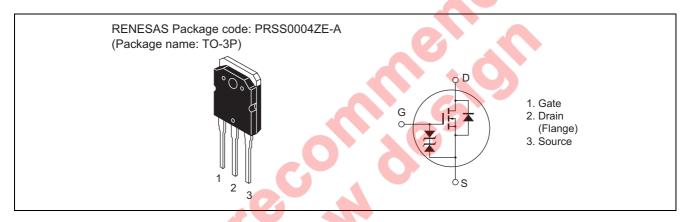
Rev.3.00

Sep 07, 2005

Features

- Low on-resistance
- High speed switching
- Low drive current
- Avalanche ratings

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	500	V	
Gate to source voltage	V_{GSS}	±30	V	
Drain current	I _D	10	А	
Drain peak current	I _{D(pulse)} *1	40	А	
Body to drain diode reverse drain current	I _{DR}	10	A	
Avalanche current	I _{AP} *3	10	A	
Avalanche energy	E _{AR} * ³	5.55	mJ	
Channel dissipation	Pch*2	100	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	−55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. Value at $Tc = 25^{\circ}C$

3. Value at Tch = 25°C, Rg \geq 50 Ω

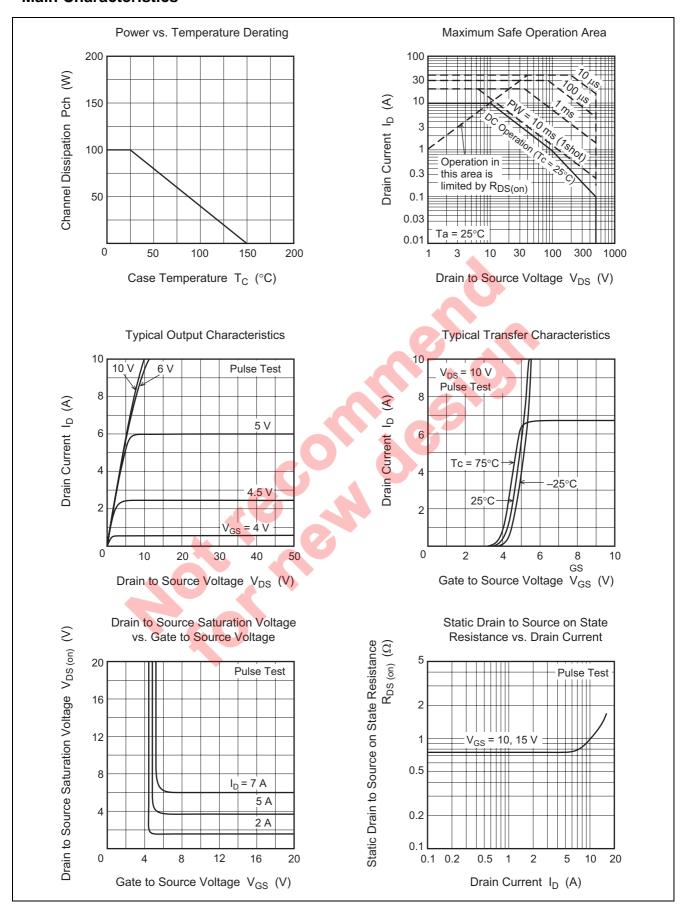
Electrical Characteristics

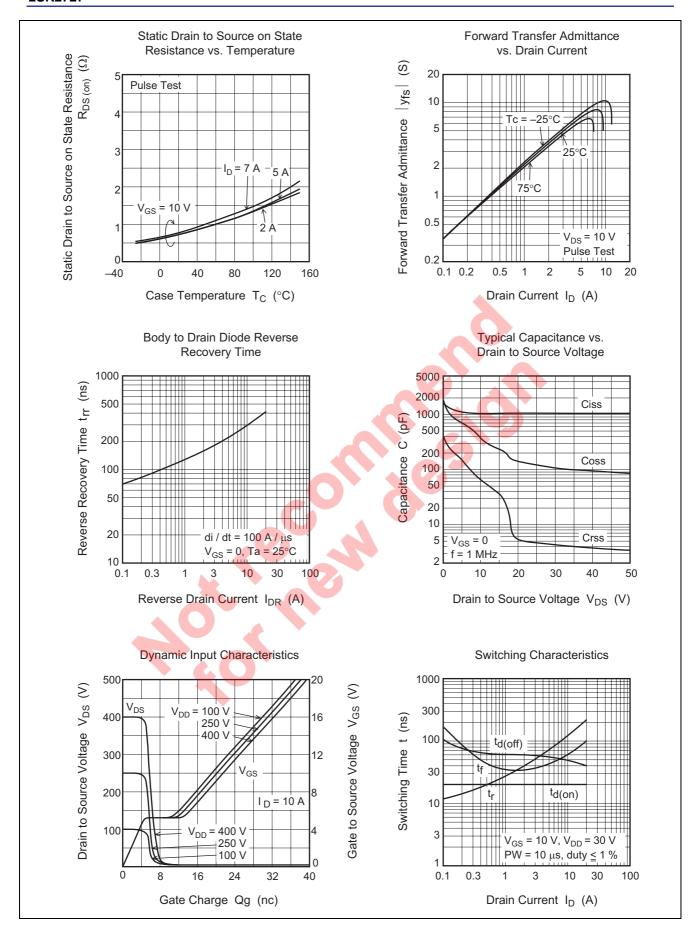
 $(Ta = 25^{\circ}C)$

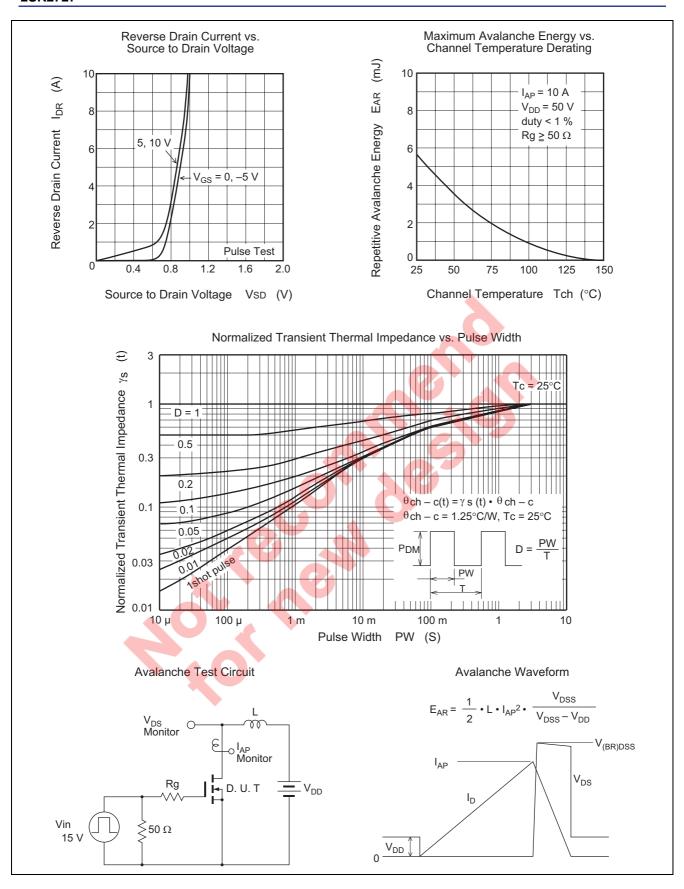
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	V _{(BR)DSS}	500	- /-		V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30		_	V	$I_{G} = \pm 100 \mu\text{A}, V_{DS} = 0$	
Gate to source leak current	I_{GSS}	_		±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I _{DSS}	_	Ė	10	μΑ	$V_{DS} = 500 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	2.5	—	3.5	V	$I_D = 1 \text{m A}, V_{DS} = 10 \text{ V}^{*4}$	
Static drain to source on state	R _{DS(on)}		0.75	0.95	Ω	$I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}^{*4}$	
resistance		5					
Forward transfer admittance	y _{fs}	4.2	7.0		S	$I_D = 5 \text{ A}, V_{DS} = 10 \text{ V}^{*4}$	
Input capacitance	Ciss	_	1100	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$	
Output capacitance	Coss	-	330	_	pF	f = 1 MHz	
Reverse transfer capacitance	Crss		65	_	pF		
Total gate charge	Qg	-	21	_	nc	$V_{DD} = 400 \text{ V}, V_{GS} = 10 \text{ V},$	
Gate to source charge	Qgs	_	5	_	nc	I _D = 10 A	
Gate to drain charge	Qgd	_	8	_	nc		
Turn-on delay time	t _{d(on)}	_	20	_	ns	$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A},$	
Rise time	t _r	_	70	_	ns	$R_L = 6 \Omega$	
Turn-off delay time	t _{d(off)}	_	55	_	ns		
Fall time	t _f	_	50	_	ns		
Body to drain diode forward voltage	V_{DF}	_	1.0	_	V	$I_D = 10A, V_{GS} = 0$	
Body to drain diode reverse recovery time	t _{rr}	_	300	_	ns	$I_F = 10A$, $V_{GS} = 0$ $di_F/dt = 100 A/\mu s$	
Note: 4 Dides to t						air, at = 100 / 1/40	

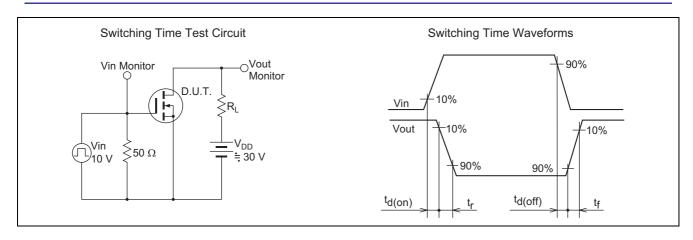
Note: 4. Pulse test

Main Characteristics



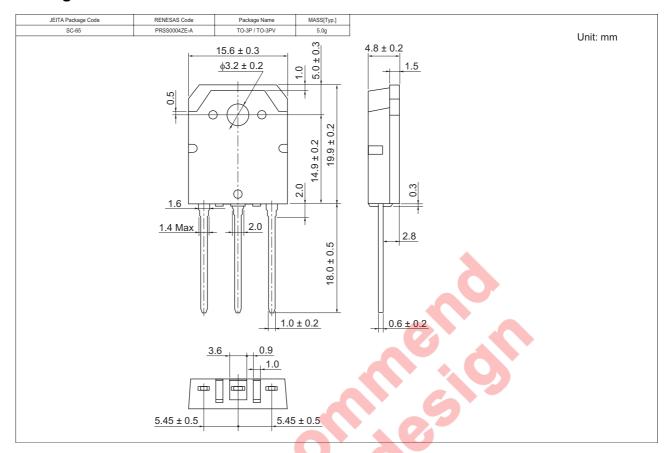








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK2727-E	360 pcs	Box (Tube)

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