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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR μ PA1915

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The μ PA1915 is a switching device which can be driven directly by a 2.5-V power source.

The μ PA1915 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- · Can be driven by a 2.5-V power source
- · Low on-state resistance

RDS(on)1 = 55 m Ω MAX. (VGS = -4.5 V, ID = -2.5 A)

 $R_{DS(on)2} = 58 \text{ m}\Omega \text{ MAX.}$ (Vgs = -4.0 V, ID = -2.5 A)

RDS(on)3 = 82 m Ω MAX. (VGS = -2.7 V, ID = -2.5 A)

 $R_{DS(on)4} = 90 \text{ m}\Omega \text{ MAX}. \text{ (Vgs} = -2.5 \text{ V}, I_D = -2.5 \text{ A})$

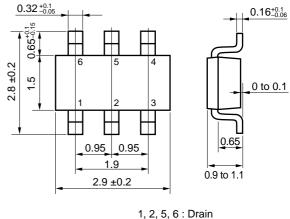
ORDERING INFORMATION

PART NUMBER	PACKAGE	
μPA1915TE	SC-95 (Mini Mold Thin Type)	

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

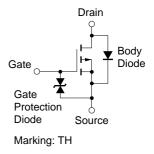
Drain to Source Voltage	VDSS	-20	V
Gate to Source Voltage	Vgss	±12	V
Drain Current (DC)	I _{D(DC)}	±4.5	Α
Drain Current (pulse) Note1	I _{D(pulse)}	±18	Α
Total Power Dissipation	P _{T1}	0.2	W
Total Power Dissipation Note2	P _{T2}	2	W
Channel Temperature	T_ch	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

PACKAGE DRAWING (Unit: mm)



1, 2, 5, 6 : Drain 3 : Gate 4 : Source

EQUIVALENT CIRCUIT



Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1 %

2. Mounted on FR-4 Board, $t \le 5$ sec.

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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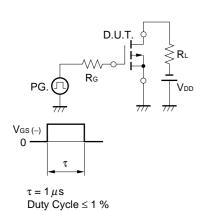
Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

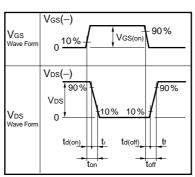


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

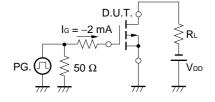
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -20 V, V _{GS} = 0 V			-10	μΑ
Gate Leakage Current	lgss	Vgs = ±12 V, Vps = 0 V			±10	μΑ
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1 mA	-0.5	-1.1	-1.5	V
Forward Transfer Admittance	yfs	V _{DS} = -10 V, I _D = -2.5 A	3	8.8		S
Drain to Source On-state Resistance	R _{DS(on)1}	Vgs = -4.5 V, ID = -2.5 A		45	55	mΩ
	R _{DS(on)2}	Vgs = -4.0 V, ID = -2.5 A		47	58	mΩ
	RDS(on)3	Vgs = -2.7 V, ID = -2.5 A		61	82	mΩ
	R _{DS(on)4}	Vgs = -2.5 V, ID = -2.5 A		67	90	mΩ
Input Capacitance	Ciss	V _{DS} = -10 V		820		pF
Output Capacitance	Coss	V _G s = 0 V		210		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		100		pF
Turn-on Delay Time	td(on)	Vpp = -10 V		16		ns
Rise Time	tr	I _D = -2.5 A		14		ns
Turn-off Delay Time	td(off)	$V_{GS(on)} = -4.0 \text{ V}$		58		ns
Fall Time	tr	$R_G = 10 \Omega$		46		ns
Total Gate Charge	Q _G	Vpp = -16 V		5.0		nC
Gate to Source Charge	Qgs	I _D = -4.5 A		2.0		nC
Gate to Drain Charge	Q _{GD}	Vgs = -4.0 V		2.5		nC
Diode Forward Voltage	V _{F(S-D)}	IF = 4.5 A, VGS = 0 V		0.86		V

TEST CIRCUIT 1 SWITCHING TIME

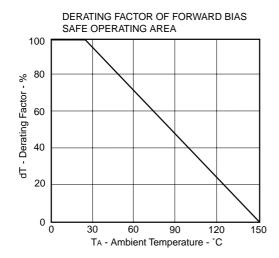


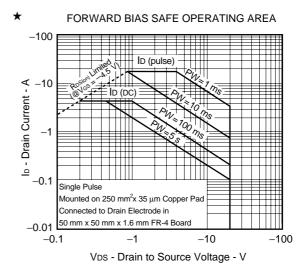


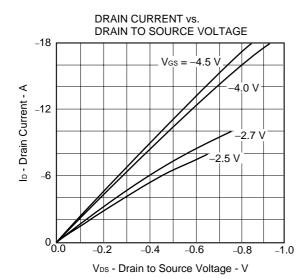
TEST CIRCUIT 2 GATE CHARGE

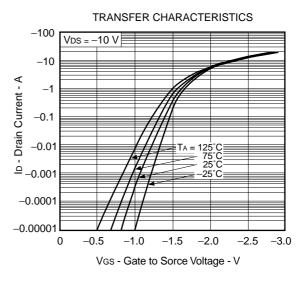


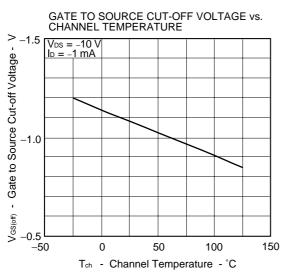
TYPICAL CHARACTERISTICS (TA = 25°C)

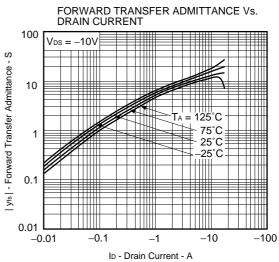






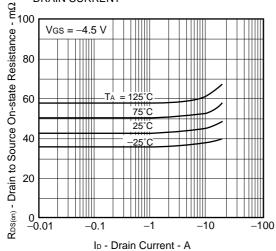




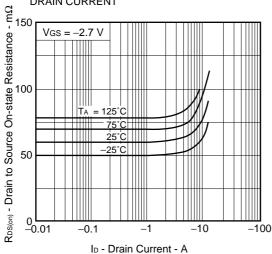


3

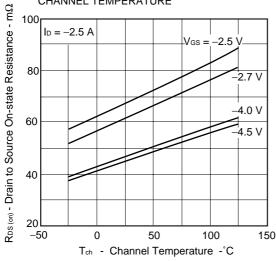
DRAIN TO SOURCE ON-STATE RESISTANCE vs. **DRAIN CURRENT**



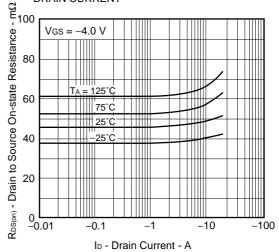
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



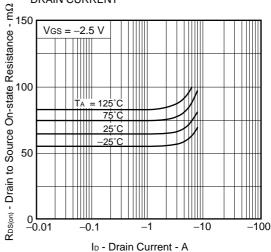
DRAIN TO SOURCE ON STATE RESISTANCE vs. CHANNEL TEMPERATURE



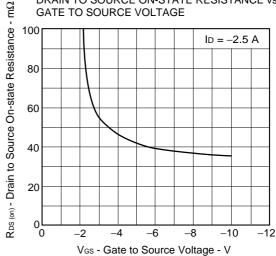
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

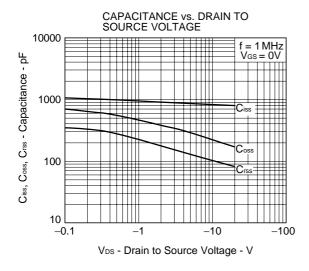


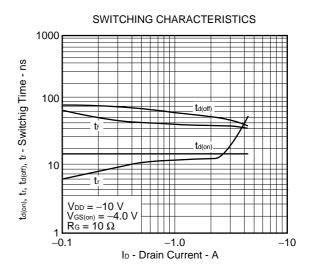
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



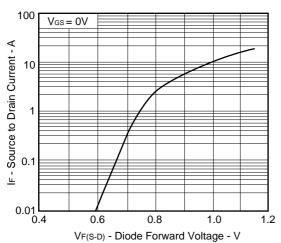
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

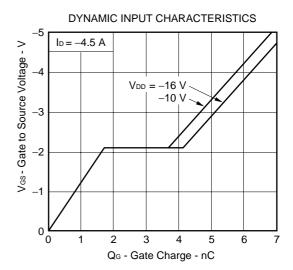


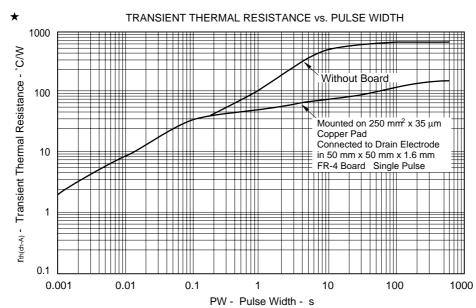




SOURCE TO DRAIN DIODE FORWARD VOLTAGE







5

NEC μ PA1915

[MEMO]

NEC μ PA1915

[MEMO]

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