LED + Photo-IC TOSHIBA Photocoupler

TLX9309

- Inverter Control Applications
- Interface for Intelligent Power Modules
- O HEV (Hybrid Electric Vehicle) and EV (Electric Vehicle) Applications

The TOSHIBA TLX9309 mini-flat photocoupler is suitable for surface-mount assembly.

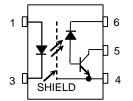
The TLX9309 consists of a high-output light emitting diode optically coupled to a high-speed detector, which consists of a photodiode and a transistor integrated on a single chip.

Faraday shield integrated on the photodetector chip provides an enhanced common-mode transient immunity.

The TLX9309 guarantees minimum and maximum propagation delay time, propagation delay difference between tpHL and tpLH, and high common-mode transient immunity.

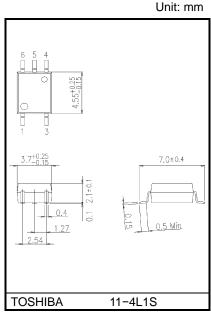
Therefore, the TLX9309 is suitable for isolation interface between an IPM (intelligent power module) and a control IC circuit in motor control applications.





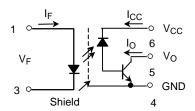
- 1: Anode
- 3: Cathode
- 4 : Emitter (GND)
- 5 : Collector (Output)
- 6: V_{CC}

- Common mode transient immunity: ±10 kV/µs (min) @VCM=1500V
- Propagation delay time: tpHL, tpLH = 0.1 μs (min), tpHL = $0.8 \mu s$ (max), tpLH = $1.0 \mu s$ (max) @IF = 7 mA, VCC = 15 V, RL=20 k Ω , Ta=25°C
- Propagation delay difference: 0.7 μs (max) (|tpLH-tpHL|) @IF = 7 mA, VCC = 15 V, RL=20 k Ω , Ta=25°C
- TTL-compatible



Weight: 0.08 g (typ.)

Schematic





Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

	Characteristic		Symbol	Rating	Unit
Forward current		(Note 1)	lF	15	mA
	Pulse forward current	(Note 2)	IFP	30	mA
LED	Peak transient forward current	(Note 3)	IFPT	1	Α
	Reverse voltage		VR	5	V
	Input power dissipation		PD	50	mW
	Output current		lo	25	mA
jo	Peak output current		IOP	50	mA
Detector	Output voltage		Vo	-0.5 to 20	V
۵	Supply voltage		Vcc	-0.5 to 30	V
	Output power dissipation	(Note 4)	Ро	100	mW
Stor	Storage temperature range			−55 to 150	°C
Оре	Operating temperature range			-40 to 125	°C
Lea	Lead soldering temperature(10 s)			260	°C
Isola	ation voltage(AC, 60 s., R.H.≤60%, Ta=25°C)	BVS	3750	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Derate 0.14 mA / °C above 70 °C.

Note 2: 50% duty cycle, 1ms pulse width. Derate 0.29 mA / $^{\circ}$ C above 70 $^{\circ}$ C.

Note 3: Pulse width PW $\leq 1 \mu s$, 300pps.

Note 4: Derate 1.8 mW / °C above 70 °C.

Note 5: Device considered a two-terminal device: Pins1 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

Recommended Operating Conditions (Note)

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	Vcc	_	_	30	V
Output voltage	Vo	_	_	20	V
Forward current	lF	_	7	12	mA
Operating temperature (Note 1)	T _{opr}	-40	_	125	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 1: Denotes the operating range, not the recommended operating condition.



Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristic		Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	VF	IF= 7 mA	1.35	1.60	1.75	V
ED	Forward voltage temperature coefficient	ΔV _F / ΔTa	I _F = 7 mA Ta = -40 to 125 °C	_	-2.0	_	mV / °C
=	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance between terminal	Ст	V = 0 V, f = 1MHz, Ta = 25 °C	_	50	_	pF
	High level output current	IOH(1)	IF = 0 mA, VCC = VO = 5.5 V	_	_	500	nA
		I _{OH(2)}	I _F = 0 mA, V _{CC} = 30 V V _O = 20 V	_	_	5	
Detector		Іон	I _F = 0 mA, V _{CC} = 30 V V _O = 20 V, Ta = 125 °C	_	_	100	μΑ
	High level supply current	Іссн	I _F = 0 mA, V _{CC} = 30 V	_	0.01	1	μА
	Supply voltage	Vcc	ICC = 0.01 mA	30	_	_	V
	Output voltage	Vo	I _O = 0.5 mA	20	_	_	V

Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	IO/IF	IF= 7 mA, V _{CC} = 4.5 V V _O = 0.4 V	15	_	300	%
Current transfer ratio		I _F = 7 mA, V _{CC} = 4.5 V V _O = 0.4 V, Ta = -25 to 100 °C	10	_	_	
		I _F = 7 mA, V _{CC} = 4.5 V V _O = 0.4 V, Ta = -40 to100 °C	8	_	-	
Low level output voltage	VoL	IF = 7 mA, V _{CC} = 4.5 V I _O = 2.4 mA	_	_	0.4	V

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output (Note 1)	Cs	V = 0 V, f = 1 MHz	_	0.6	_	pF
Isolation resistance (Note 1)	Rs	R.H. ≤ 60%, V _S = 500 V	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage (Note 1)	BVS	AC, 60 s	3750	-	_	Vrms

Note 1: Device considered a two-terminal device: Pins1 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.



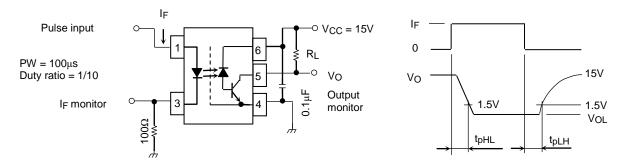
Switching Characteristics (Unless otherwise specified, Ta = 25°C, Vcc = 15V)

Characteristic	Symbol	Test Cir- Cuit	Test Condition	Min.	Тур.	Max.	Unit
	t _p HL		I _F = 0 \rightarrow 7 mA, R _L = 20 kΩ	0.1	0.35	0.8	
Propagation delay time (H→ L)			I _F = 0 \rightarrow 7 mA, R _L = 20 kΩ Ta = 0 to 85 °C	0.1	_	0.9	μS
((1→ L)			I _F = 0 \rightarrow 7 mA, R _L = 20 kΩ Ta = -40 to 100 °C	0.05		1.0	
			$I_F=7→0$ mA, $R_L=20$ kΩ	0.1	0.45	1.0	
Propagation delay time	tpLH	1	I _F = 7 \rightarrow 0 mA, R _L = 20 kΩ Ta = 0 to 85 °C	0.1	_	1.1	μS
(L→ H)			I _F = 7 \rightarrow 0 mA, R _L = 20 kΩ Ta = -40 to 100 °C	0.05	_	1.2	
	t _{pLH} -t _{pHL}		I_F = 7 mA, R_L = 20 k Ω	_	0.1	0.7	
Propagation delay difference			I _F = 7 mA, R _L = 20 kΩ Ta = 0 to 85 °C	_	_	1.0	μS
			I _F = 7 mA, R _L = 20 kΩ Ta = - 40 to 100 °C	_	_	1.1	
Common mode transient immunity at logic high output (Note 1)	СМн	2	I_F = 0 mA V_{CM} = 1500 V_{p-p} R_L = 20 k Ω	10000	15000		V / μs
Common mode transient immunity at logic low output (Note 1)	CML		I _F = 7 mA V_{CM} = 1500 V_{p-p} R_L = 20 k Ω	-10000	-15000	_	V / μs

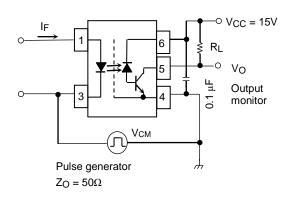
Note 1: CML is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic low state (Vo<1.0V).

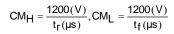
CMH is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic high state (Vo>11.0V).

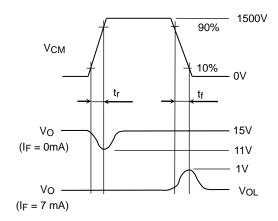
Test Circuit 1: Switching Time Test Circuit

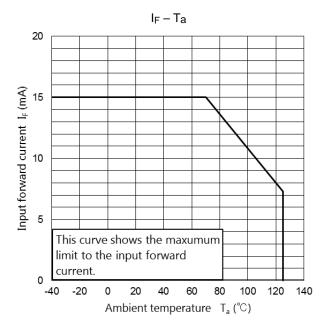


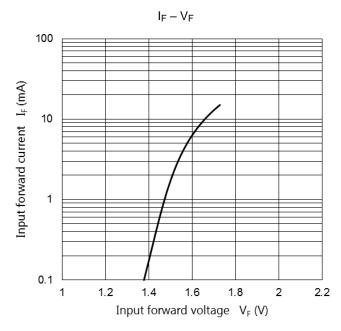
Test Circuit 2: Common Mode Noise Immunity Test Circuit

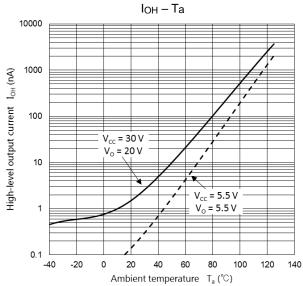


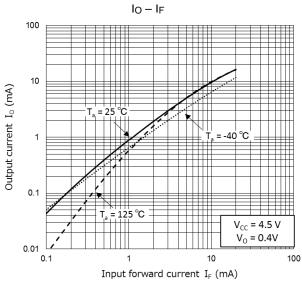


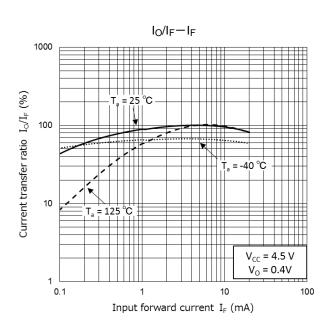


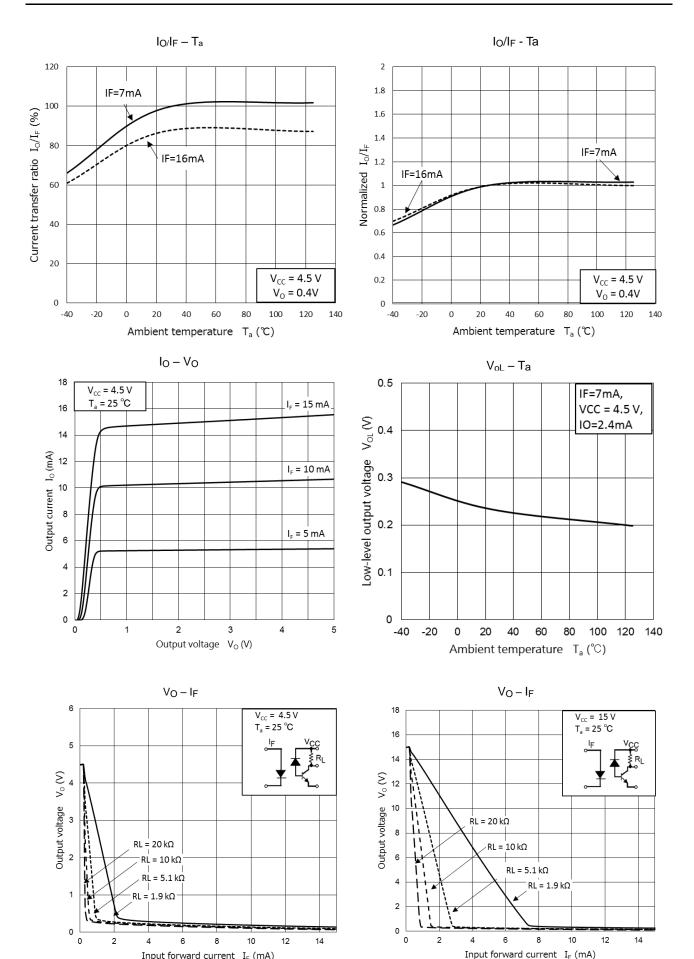






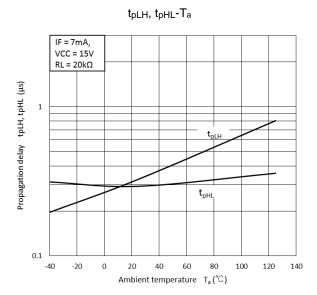


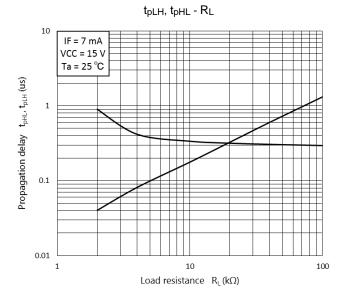




Input forward current I_F (mA)

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TLX9309

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise specified.



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