

Fast switching diode chip in Emitter Controlled 3 -Technology

Features:

- 600V Emitter Controlled 3 technology • 70 µm chip
- soft, fast switching ٠
- low reverse recovery charge
- small temperature coefficient
- This chip is used for: •
- Power module



Applications:

Drives

Chip Type	V _R	I _F	Die Size	Package
SIDC26D60C8	600V	100A	6.53 x 4.02 mm ²	sawn on foil

Mechanical Parameters

Meenanical Farameters			
Raster size	6.53 x 4.02		
Area total	26.25	mm ²	
Anode pad size	5.83 x 3.32		
Thickness	70	μm	
Wafer size	200	mm	
Max. possible chips per wafer	1032		
Passivation frontside Photoimide			
Pad metal	3200 nm AlSiCu		
Backside metal	Ni Ag –system suitable for epoxy and soft solder die bonding		
Die bond	Electrically conductive glue or solder		
√ire bond Al, ≤500μm			
Reject ink dot size	Ø 0.65mm; max 1.2mm		
Recommended storage environment	Store in original container, in dry nitrogen, in dark environment, < 6 month at an ambient temperature of 23°C		



Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V _{RRM}	<i>T</i> _{vj} = 25 ℃	600	V
Continuous forward current	I _F	<i>T</i> _{vj} < 150℃	1)	Δ
Maximum repetitive forward current	I _{FRM}	<i>T</i> _{vj} < 150℃	200	A
Junction temperature range	T _{vj}		-40+175	°C
Operating junction temperature	T _{vj}		-40+150	°C
Dynamic ruggedness ²⁾	P _{max}	$I_{\rm Fmax} = 200 {\rm A}, \ V_{\rm Rmax} = 600 {\rm V}, \\ T_{\rm vj} \leq 150 {\rm C}$	tbd	kW

¹⁾ depending on thermal properties of assembly

²) not subject to production test - verified by design/characterisation

Static Characteristics (tested on wafer), $T_{vj} = 25 \ ^{\circ}{\rm C}$

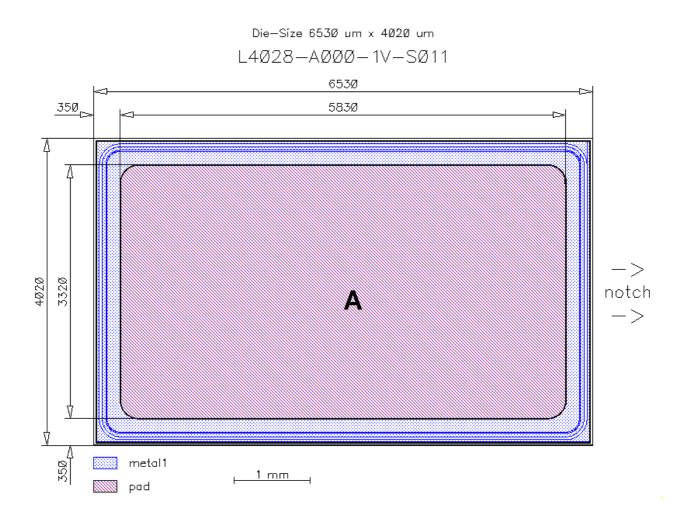
Parameter	Symbol	Conditions	Value			Unit
Falametei	Symbol	Conditions	min.	typ.	max.	Onne
Reverse leakage current	I _R	V _R =600V			27	μA
Cathode-Anode breakdown Voltage	V _{BR}	/ _R =0.25mA	600			V
Diode forward voltage	V _F	$I_{\rm F} = 100 {\rm A}$	1.2	1.6	1.9	V

Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.



Chip Drawing



A: Anode pad



Description

AQL 0,65 for visual inspection according to failure catalogue

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Revision History

Version	Subjects (major changes since last revision)	Date

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