

HSC285

Silicon Schottky Barrier Diode for Detector

REJ03G0011-0200
Rev.2.00
May 17, 2006

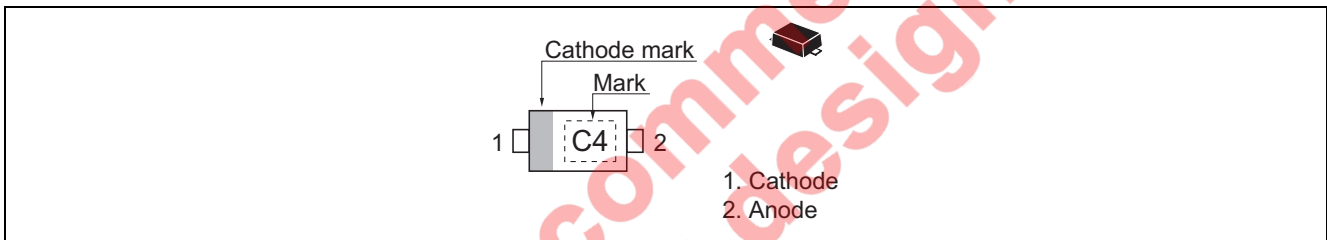
Features

- Low forward voltage, Low capacitance and High detection sensitivity.
- Ultra small Flat Lead Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HSC285	C4	UFP	PWFS0002ZA-A

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V _R	2	V
Average rectified current	I _O	5	mA
Junction temperature	T _j	125	°C
Storage temperature	T _{stg}	-55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V _{F1}	—	—	0.15	V	I _F = 0.1 mA
	V _{F2}	—	—	0.27		I _F = 1 mA
Capacitance	C	—	0.3	—	pF	V _R = 0.5 V, f = 1 MHz
ESD-Capability *1	—	10	—	—	V	C = 200 pF, R _L = 0 Ω, Both forward and reverse direction 1 pulse.

Note: 1. Failure criterion ; I_R ≥ 100 μA at V_R = 0.5 V

Not recommended
for new design

Main Characteristic

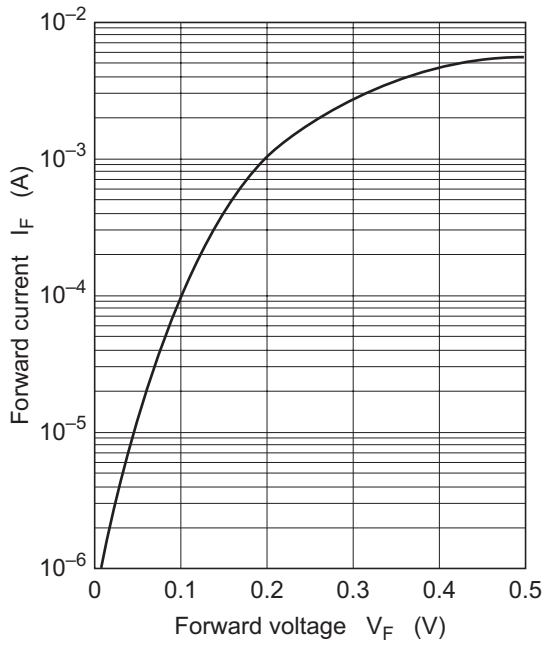


Fig.1 Forward current vs. Forward voltage

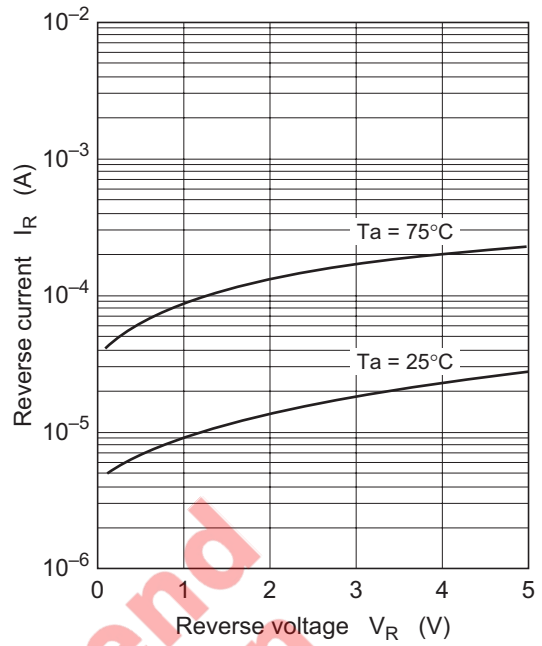


Fig.2 Reverse current vs. Reverse voltage

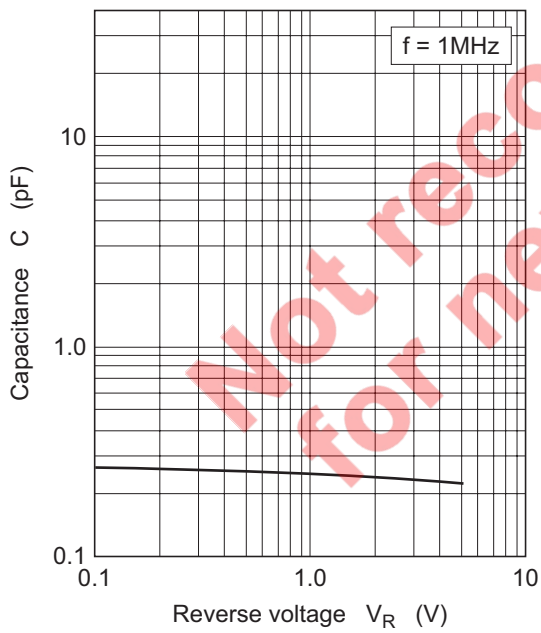
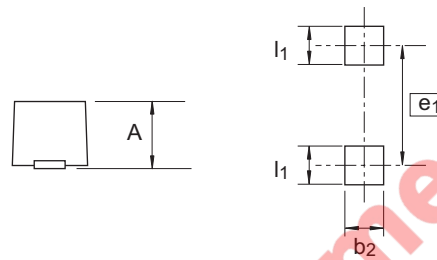
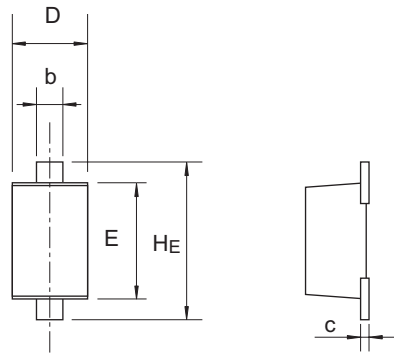


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
UFP	SC-79	PWSF0002ZA-A	UFP / UFPV	0.0016g



Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.50	0.60	0.70
b	0.25	0.30	0.35
c	0.08	0.13	0.18
D	0.70	0.80	0.90
E	1.10	1.20	1.30
HE	1.50	1.60	1.70
b2	—	0.80	—
e1	—	1.70	—
l1	—	0.60	—

Not recommend for new design

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April 1st, 2010
Renesas Electronics Corporation

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Not recommended
for new design

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