# DISCRETE SEMICONDUCTORS

# DATA SHEET

PIMH9; PUMH9; PEMH9 NPN/NPN resistor-equipped transistors; R1 = 10 kΩ, R2 = 47 kΩ

Product specification Supersedes data of 2003 Sep 15 2004 Apr 14





# NPN/NPN resistor-equipped transistors; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$

# PIMH9; PUMH9; PEMH9

### **FEATURES**

- Built-in bias resistors
- · Simplifies circuit design
- Reduces component count
- · Reduces pick and place costs.

### **APPLICATIONS**

- · General purpose switching and amplification
- · Inverter and interface circuits
- · Circuit driver.

### QUICK REFERENCE DATA

SYMBOL	PARAMETER	TYP.	MAX.	UNIT
V <sub>CEO</sub>	collector-emitter voltage	_	50	V
Io	output current (DC)	_	100	mA
TR1	NPN	_	_	-
TR2	NPN	_	_	_
R1	bias resistor	10	_	kΩ
R2	bias resistor	47	_	kΩ

#### **DESCRIPTION**

NPN/NPN resistor-equipped transistor (see "Simplified outline, symbol and pinning" for package details).

### PRODUCT OVERVIEW

TYPE NUMBER	PACKAGE MARKING CODE		PACKAGE		MARKING CODE	PNP/PNP	NPN/PNP
I I PE NOMBER	PHILIPS	EIAJ	WARKING CODE	COMPLEMENT	COMPLEMENT		
РЕМН9	SOT666	_	H9	PEMB9	PEMD9		
PIMH9	SOT457	SC-74	H9	_	_		
PUMH9	SOT363	SC-88	H*9 <sup>(1)</sup>	PUMB9	PUMD9		

### Note

- 1. \* = p: Made in Hong Kong.
  - \* = t: Made in Malaysia.
  - \* = W: Made in China.

## SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PINNING		
I TPE NUMBER	SIMPLIFIED OUTLINE AND STMBOL	PIN	DESCRIPTION	
PEMH9		1	emitter TR1	
PIMH9	□6 □5 □4 <u>6 5 4</u>	2	base TR1	
PUMH9		3	collector TR2	
	R1 R2 R2	4	emitter TR2	
	TR1	5	base TR2	
		6	collectorTR1	
	1 2 3			
	Top view MHC049			

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# ORDERING INFORMATION

TYPE NUMBER		PACKAGE	
TYPE NUMBER NAME		DESCRIPTION	VERSION
PEMH9	_	plastic surface mounted package; 6 leads	SOT666
PIMH9	<ul> <li>plastic surface mounted package; 6 leads</li> </ul>		SOT457
PUMH9	_	plastic surface mounted package; 6 leads	SOT363

## **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transist	or		·		
V <sub>CBO</sub>	collector-base voltage open emitter -		_	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	10	V
Vi	input voltage positive negative			+40 -10	V
Io	output current		_	100	mA
I <sub>CM</sub>	peak collector current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C			
	SOT363	note 1	_	200	mW
	SOT457	note 1	_	300	mW
	SOT666	notes 1 and 2	_	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C
Per device			·		
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C			
	SOT363	note 1	_	300	mW
	SOT457	note 1	_	600	mW
	SOT666	notes 1 and 2	_	300	mW

### **Notes**

- 1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
- 2. Reflow soldering is the only recommended soldering method.

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# THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
Per transist	or			
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	T <sub>amb</sub> ≤ 25 °C		
	SOT363	note 1	625	K/W
	SOT457	note 1	417	K/W
	SOT666	notes 1 and 2	625	K/W
Per device				
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	T <sub>amb</sub> ≤ 25 °C		
	SOT363	note 1	416	K/W
	SOT457	note 1	208	K/W
	SOT666	notes 1 and 2	416	K/W

### **Notes**

- 1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
- 2. Reflow soldering is the only recommended soldering method.

### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A	_	_	100	nA
I <sub>CEO</sub>	collector-emitter cut-off current	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A	_	_	1	μΑ
		$V_{CE} = 30 \text{ V}; I_{B} = 0 \text{ A}; T_{j} = 150 ^{\circ}\text{C}$	_	_	50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}$	_	_	150	μΑ
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 5 \text{ mA}$	100	_	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 5 \text{ mA}; I_B = 0.25 \text{ mA}$	_	_	100	mV
V <sub>i(off)</sub>	input-off voltage	$V_{CE} = 5 \text{ V}; I_{C} = 100 \mu\text{A}$	_	0.7	0.5	V
V <sub>i(on)</sub>	input-on voltage	$V_{CE} = 0.3 \text{ V}; I_{C} = 1 \text{ mA}$	1.4	0.8	_	V
R1	input resistor		7	10	13	kΩ
R2 R1	resistor ratio		3.7	4.7	5.7	
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz	_	_	2.5	pF

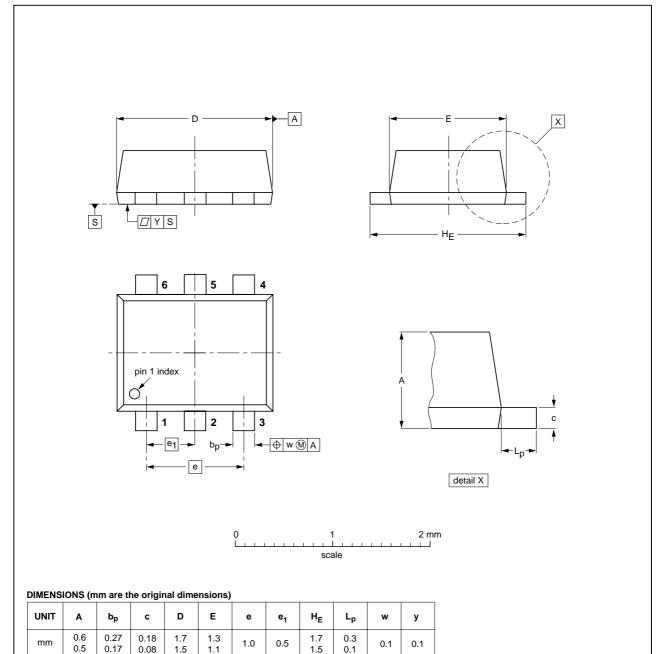
# NPN/NPN resistor-equipped transistors; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$

PIMH9; PUMH9; PEMH9

## **PACKAGE OUTLINES**

## Plastic surface mounted package; 6 leads

SOT666



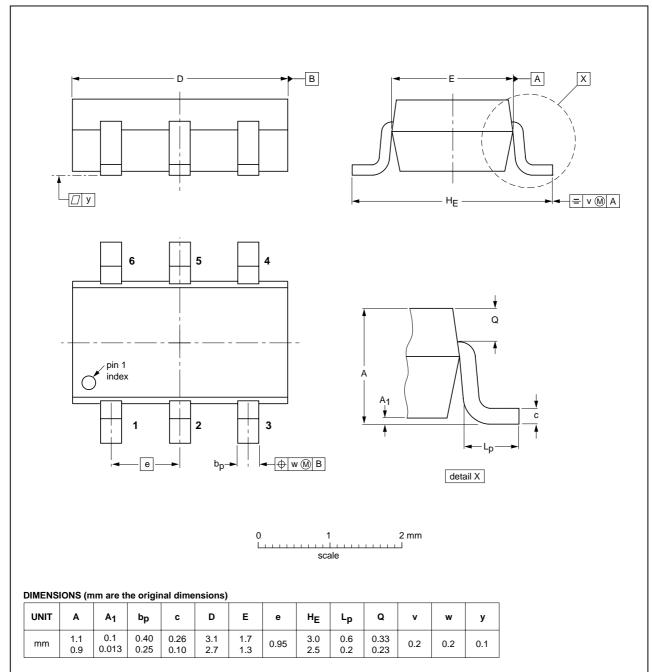
OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT666						<del>-01-01-04</del> 01-08-27

# NPN/NPN resistor-equipped transistors; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$

# PIMH9; PUMH9; PEMH9

## Plastic surface mounted package; 6 leads

SOT457



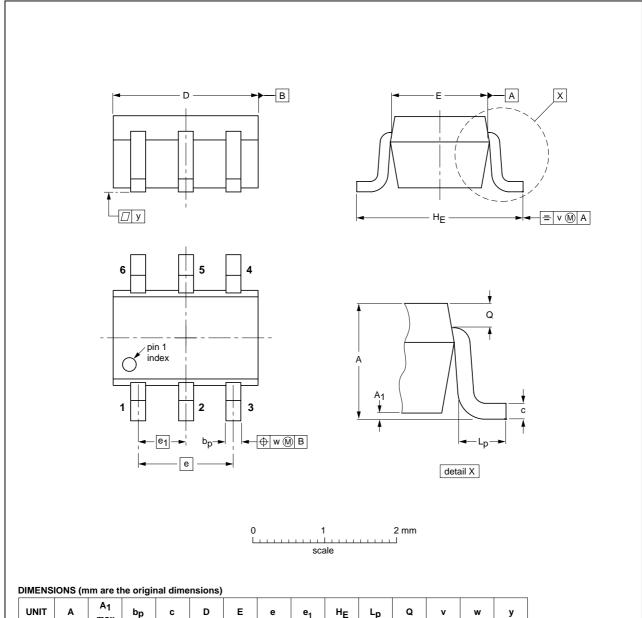
	REFER	RENCES	EUROPEAN	ISSUE DATE	
IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
		SC-74			<del>97-02-28</del> 01-05-04
	IEC			IEC JEDEC EIAJ	IEC JEDEC EIAJ PROJECTION

# NPN/NPN resistor-equipped transistors; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$

# PIMH9; PUMH9; PEMH9

## Plastic surface mounted package; 6 leads

SOT363



UNIT	Α	A <sub>1</sub> max	bp	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	v	w	у
mm	1.1 0.8	0.1	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT363			SC-88			97-02-28

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#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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#### **DEFINITIONS**

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