Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
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SILICON TRANSISTOR ARRAY

μPA1428A

NPN SILICON POWER TRANSISTOR ARRAY HIGH SPEED SWITCHING USE (DARLINGTON TRANSISTOR) INDUSTRIAL USE

DESCRIPTION

The μ PA1428A is NPN silicon epitaxial Darlington Power Transistor Array that built in Surge Absorber 4 circuits designed for driving solenoid, relay, lamp and so on.

FEATURES

- · Surge Absorber built in.
- · Easy mount by 0.1 inch of terminal interval.
- High hee for Darlington Transistor.

ORDERING INFORMATION

Part Number	Package	Quality Grade		
μPA1428AH	10 Pin SIP	Standard		

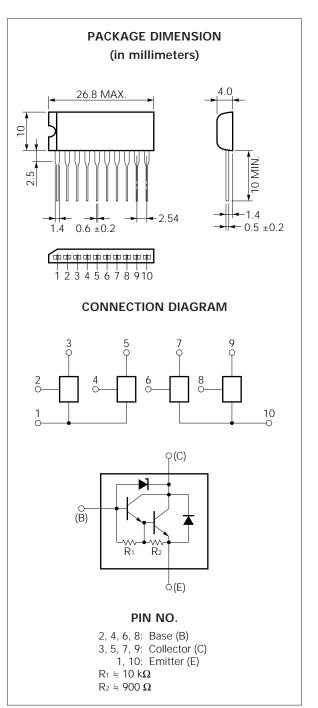
Please refer to "Quality grade on NEC Semiconductor Device" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

Collector to Base Voltage	VcBo	60 ±10	V		
Collector to Emitter Voltage	V_{CEO}	60 ±10	V		
Emitter to Base Voltage	V_{EBO}	8	V		
Surge Sustaining Energy	Eceo(sus)	30	mJ/unit		
Collector Current (DC)	Ic(DC)	±2	A/unit		
Collector Current (pulse)	Ic(pulse)*	±3	A/unit		
Base Current (DC)	IB(DC)	0.2	A/unit		
Total Power Dissipation	P _{T1} **	3.5	W		
Total Power Dissipation	P _{T2} ***	28	W		
Junction Temperature	Tj	150	.C		
Storage Temperature	Tstg -5	55 to +15	0 °C		
* PW \leq 350 μ s, Duty Cycle \leq 2 %					

** 4 Circuits, Ta = 25 °C

*** 4 Cuircuits, Tc = 25 °C



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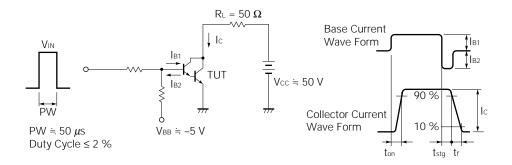


ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Leakage Current	Ісво			1	μΑ	Vcb = 40 V, IE = 0
Emitter Leakage Current	Івво			5	mA	V _{EB} = 5 V, I _C = 0
Collector to Emitter Sustaining Voltage	VCEO(sus)	50	60	70	V	Ic = 1 A, L = 1 mH
DC Current Gain	h _{FE1} *	2000		20000	_	Vce = 2 V, Ic = 1 A
DC Current Gain	h _{FE2} *	500			_	Vce = 2 V, Ic = 2 A
Collector Saturation Voltage	VCE(sat) *		1.0	1.5	V	Ic = 1 A, I _B = 1 mA
Base Saturation Voltage	V _{BE(sat)} *		1.7	2	V	Ic = 1 A, I _B = 1 mA
Turn On Time	ton		0.4		μs	Ic = 1 A
Storage Time	tstg		1.5		μs	$\begin{array}{lll} I_{B1} = -I_{B2} = 2 \text{ mA} \\ V_{CC} \doteq 50 \text{ V}, \text{ RL} = 50 \Omega \\ \text{See test circuit} \end{array}$
Fall Time	tr		0.4		μs	

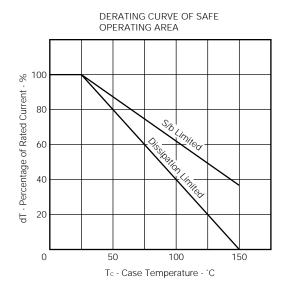
^{*} PW \leq 350 μ s, Duty Cycle \leq 2 %/pulsed

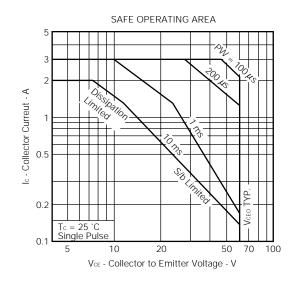
SWITCHING TIME TEST CIRCUIT

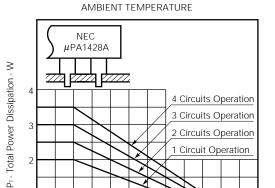




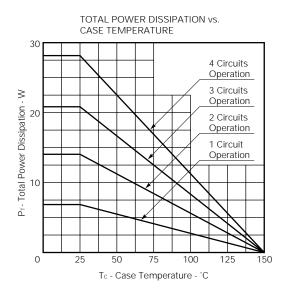
TYPICAL CHARACTERISTICS (Ta = 25 °C)

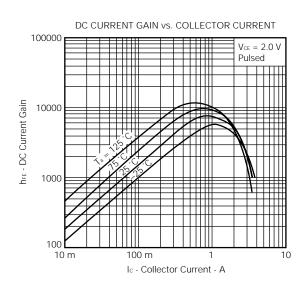




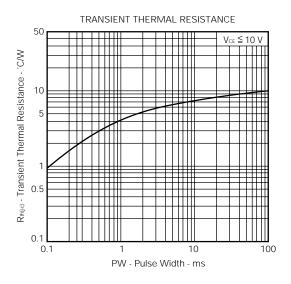


TOTAL POWER DISSIPATION vs.

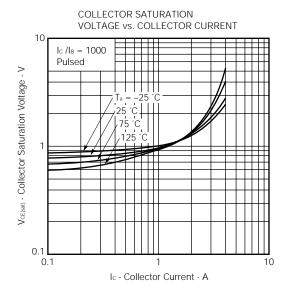


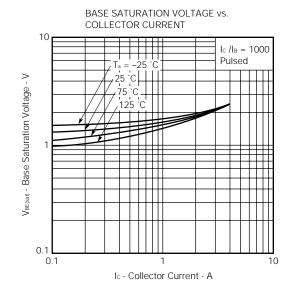


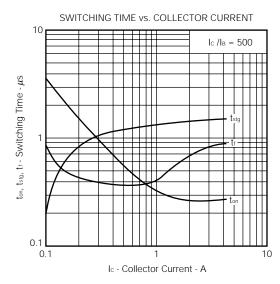
 T_a - Ambient Temperature - ${}^{\circ}C$

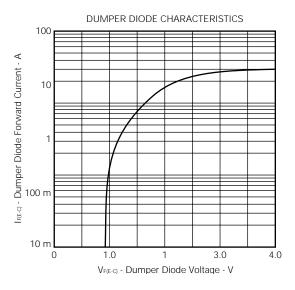














REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134

5

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