## 1-of-10 Decoder/Driver Open-Collector

The SN74LS145, 1-of-10 Decoder/Driver, is designed to accept BCD inputs and provide appropriate outputs to drive 10-digit incandescent displays. All outputs remain off for all invalid binary input conditions. It is designed for use as indicator/relay drivers or as an open-collector logic circuit driver. Each of the high breakdown output transistors will sink up to 80 mA of current. Typical power dissipation is 35 mW. This device is fully compatible with all TTL families.

- Low Power Version of 74145
- Input Clamp Diodes Limit High Speed Termination Effects

#### **GUARANTEED OPERATING RANGES**

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>CC</sub>	Supply Voltage	4.75	5.0	5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	0	25	70	°C
V <sub>OH</sub>	Output Voltage - High			15	V
I <sub>OL</sub>	Output Current - Low			24	mA



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# LOW POWER SCHOTTKY



PLASTIC N SUFFIX CASE 648



SOIC D SUFFIX CASE 751B



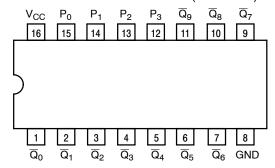
SOEIAJ M SUFFIX CASE 966

#### **ORDERING INFORMATION**

Device	Package	Shipping	
SN74LS145N	16 Pin DIP	2000 Units/Box	
SN74LS145D	SOIC-16	38 Units/Rail	
SN74LS145DR2	SOIC-16	2500/Tape & Reel	
SN74LS145M	SOEIAJ-16	See Note 1	
SN74LS145MEL	SOEIAJ-16	See Note 1	

 For ordering information on the EIAJ version of the SOIC package, please contact your local ON Semiconductor representative.

#### **CONNECTION DIAGRAM DIP (TOP VIEW)**



PIN	NAMES
	IIA

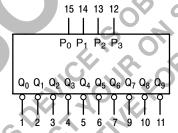
 $\begin{array}{ll} P_0,\, P_1,\, P_2,\, P_3 & \quad \text{BCD Inputs} \\ \overline{Q}_0 - \overline{Q}_9 & \quad \text{Outputs} \end{array}$ 

LOADING (Note a)						
HIGH	LOW					
0.5 U.L.	0.25 U.L.					
Open Collector	15 U.L.					

#### NOTES:

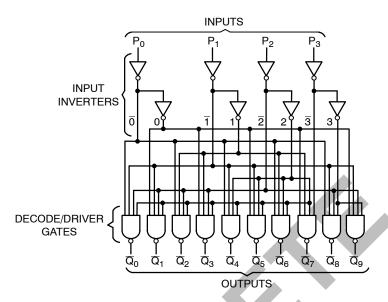
a) 1 TTL Unit Load (U.L.) = 40  $\mu$ A HIGH/1.6 mA LOW.

#### LOGIC SYMBOL



V<sub>CC</sub> = PIN 16 GND = PIN 8

#### **LOGIC DIAGRAM**



**TRUTH TABLE** 

GATES $Q_0$ $Q_1$ $Q_2$ $Q_3$ $Q_4$ $Q_5$ $Q_6$ $Q_7$ $Q_8$ $Q_9$ OUTPUTS														
						4		7 ,	Ť				10	*
							K					(0)	30	4
						RUTH	TAB	LE			C			~
	INP	UTS					7		PUTS	V .	O	N.P		
P <sub>3</sub>	P <sub>2</sub>	P <sub>1</sub>	P <sub>0</sub>	$\overline{\mathbf{Q}}_{0}$	$\overline{Q}_1$	$\overline{Q}_2$	$\overline{Q}_3$	$\overline{Q}_{4}$	$\overline{Q}_{5}$	$\overline{\mathbf{Q}}_{6}$	$\overline{Q}_{7}$	Q̄ <sub>8</sub>	Q <sub>9</sub>	
L	L	L	L	L	Н	Н	Н	G	НC	Н	Н	Н	Н	
L	L	L	H	H	L	Н	H		Н	Н	Н	Н	Н	
L	L	Н	L	Н	Н	L	H	Н	Н	H	Н	Н	Н	
L	L	Н	H	Н	Н	Н	L	Н	Н	H	Н	Н	Н	
L	Н	L	L	Н	Н	H	Н	L	Н	Н	Н	Н	Н	
L	Н	L	H	Н	H	H	Н	H	V	Н	Н	Н	Н	
L	H	H	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	
L	Н	Н	Н	Н	H	Н	Н	H	Н	Н	L	Н	Н	
H	L	L	L	H	Н	H	H	Н	Н	Н	Н	L	Н	
Н	L Ì	L	Н,	Н	Н	IJΗ,	Н	Н	Н	Н	Н	Н	L	
Н	L	Н	L	Н	H	Н	H	Н	Н	Н	Н	Н	Н	
Н	L	Н	H	H	Н	Н	Н	Н	Н	Н	Н	Н	Н	
Н	Н	L	L	H	Н	Н	Н	Н	Н	Н	Н	Н	Н	
Н	Н	L	Н	Н	H	Н	Н	Н	Н	Н	Н	Н	Н	
Н	Н	Н	,L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	
Н	Н	Н	Н	H	Н	Н	Н	Н	Н	Н	Н	Н	Н	

H = HIGH Voltage Level L = LOW Voltage Level

#### DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parameter	Min	Тур	Max	Unit	Tes	t Conditions	
V <sub>IH</sub>	Input HIGH Voltage	2.0			٧	Guaranteed Input HIGH Voltage for All Inputs		
V <sub>IL</sub>	Input LOW Voltage			0.8	٧	Guaranteed Inpu All Inputs	t LOW Voltage for	
V <sub>IK</sub>	Input Clamp Diode Voltage		-0.65	-1.5	V	V <sub>CC</sub> = MIN, I <sub>IN</sub> = –18 mA		
I <sub>OH</sub>	Output HIGH Current			250	μΑ	V <sub>CC</sub> = MIN, V <sub>OH</sub> = MAX		
			0.25	0.4	V	I <sub>OL</sub> = 12 mA	V <sub>CC</sub> = V <sub>CC</sub> MIN,	
$V_{OL}$	Output LOW Voltage		0.35	0.5	V		$V_{IN} = V_{IL}$ or $V_{IH}$	
			2.3	3.0	V	I <sub>OL</sub> = 80 mA	per Truth Table	
1	lanut IIICI I Current			20	μΑ	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 2.7 V		
I <sub>IH</sub>	Input HIGH Current			0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 7.0 V		
I <sub>IL</sub>	Input LOW Current			-0.4	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 0.4 V		
I <sub>CC</sub>	Power Supply Current			13	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub>	= GND	

#### AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

			Limits			Oly
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t <sub>PHL</sub> t <sub>PLH</sub>	Propagation Delay P <sub>n</sub> Input to Q <sub>n</sub> Output	C	0	50 50	ns	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 45 pF

#### **AC WAVEFORMS**

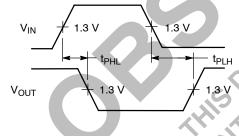


Figure 1.

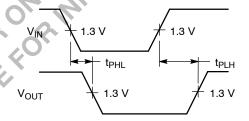
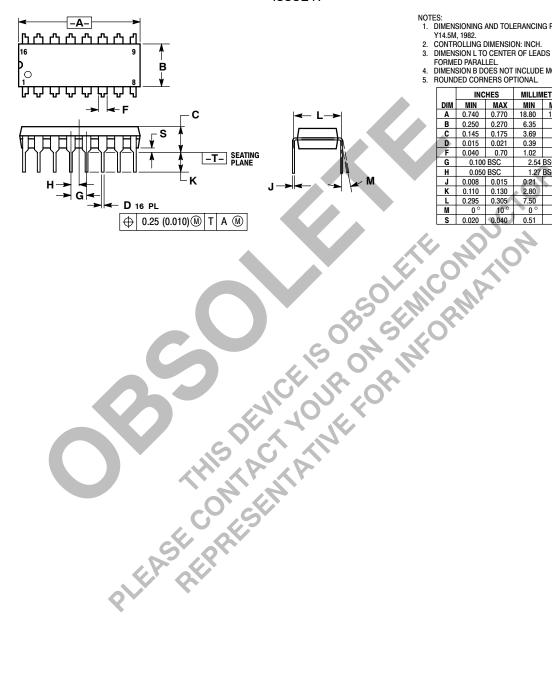


Figure 2.

#### PACKAGE DIMENSIONS

#### **N SUFFIX** PLASTIC PACKAGE CASE 648-08 **ISSUE R**



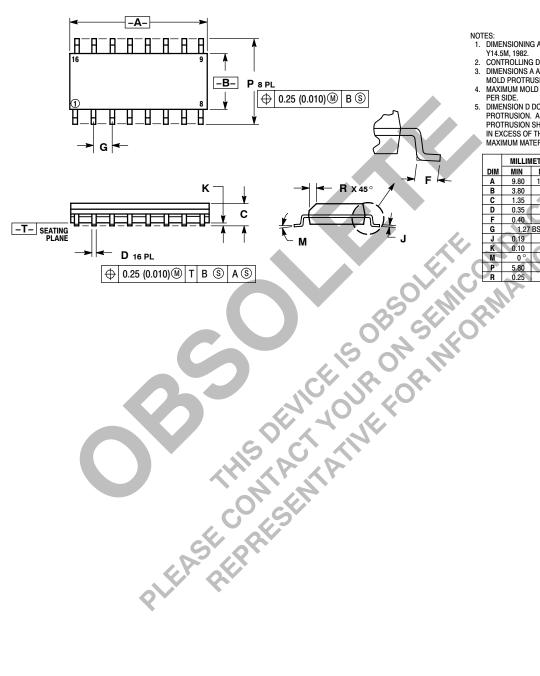
- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
- DIMENSION B DOES NOT INCLUDE MOLD FLASH.
  ROUNDED CORNERS OPTIONAL.

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
A	0.740	0.770	18.80	19.55	
В	0.250	0.270	6.35	6.85	
Ç	0.145	0.175	3.69	4.44	
Á	0.015	0.021	0.39	0.53	
F	0.040	0.70	1.02	1.77	
G	0.100	BSC	2.54 BSC		
Н	0.050	BSC	1.27 BSC		
7	0.008	0.015	0.21	0.38	
K	0.110	0.130	2.80	3.30	
L	0.295	0.305	7.50	7.74	
M	0°	10°	0 °	10°	
S	0.020	0.040	0.51	1.01	

#### PACKAGE DIMENSIONS

#### **D SUFFIX**

PLASTIC SOIC PACKAGE CASE 751B-05 **ISSUE J** 



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- Y14.5M, 1982.

  CONTROLLING DIMENSION: MILLIMETER.

  DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.

  MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.

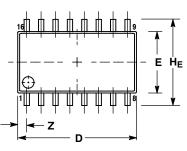
  DIMENSION D DOES NOT INCLUDE DAMBAR
- DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

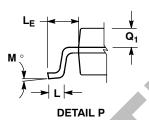
	MILLIN	IETERS	INCHES			
DIM	MIN	MAX	MIN	MAX		
Α	9.80	10.00	0.386	0.393		
В	3.80	4.00	0.150	0.157		
С	1.35	1.75	0.054	0.068		
D	0.35	0.49	0.014	0.019		
F	0.40	1.25	0.016	0.049		
G	1.27	BSC _	0.050 BSC			
J∢	0.19	0.25	0.008	0.009		
K	0.10	0.25	0.004	0.009		
M	0°	7°	0°	7°		
P	5.80	6.20	0.229	0.244		
R	0.25	0.50	0.010	0.019		

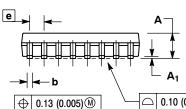
#### PACKAGE DIMENSIONS

#### **M SUFFIX**

SOEIAJ PACKAGE CASE 966-01 ISSUE O









#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER
- 3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
- TERMINAL NUMBERS ARE SHOWN FOR
  REFERENCE ONLY
- REFERENCE ONLY.

  5. THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 (0.018).

	MILLIN	IETERS	INC	HES				
DIM	MIN	MAX	MIN	MAX				
Α		2.05	-1	0.081				
Α1	0.05	0.20	0.002	0.008				
ь	0.35	0.50	0.014	0.020				
C	0.18	0.27	0.007	0.011				
D	9.90	10.50	0.390	0.413				
E	5.10	5.45	0.201	0.215				
e	1.27	BSC	0.050 BSC					
HE	7.40	8.20	0.291	0.323				
L	0.50	0.85	0.020	0.033				
LE	1.10	1.50	0.043	0.059				
M	0 °	10°	0 °	10°				
$Q_1$	0.70	0.90	0.028	0.035				
Z		0.78		0.031				

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