# SILICON TRANSISTORS 2SC2958, 2959

# NPN SILICON EPITAXIAL TRANSISTOR FOR LOW-FREQUENCY POWER AMPLIFIERS

### FEATURES

NEC

- Ideal for use of high voltage current such as TV vertical deflection (drive and output), audio output, pin cushion correction
- Complementary transistor with 2SA1221 and 2SA1222
   VCEO = 140 V: 2SA1221/2SC2958
   VCEO = 160 V: 2SA1222/2SC2959

# ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	Vсво	160	V
Collector to emitter voltage	VCEO	140/160	V
Emitter to base voltage	VEBO	5.0	V
Collector current (DC)	IC(DC)	500	mA
Collector current (pulse)	C(pulse)*	1.0	А
Total power dissipation	P⊤	1.0	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

\* PW  $\leq$  10 ms, duty cycle  $\leq$  50%

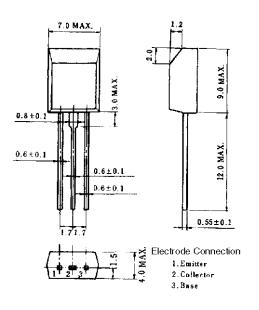
# ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions MI		TYP.	MAX.	Unit
Collector cutoff current	Ісво	$V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0$			200	nA
Emitter cutoff current	Іево	V <sub>EB</sub> = 5.0 V, Ic = 0			200	nA
DC current gain	hfe **	Vce = 2.0 V, Ic = 100 mA	100	150	400	
DC base voltage	VBE **	Vce = 5.0 V, Ic = 20 mA	0.6	0.64	0.7	V
Collector saturation voltage	VCE(sat) **	Ic = 1.0 A, I <sub>B</sub> = 0.2 A		0.32	0.7	V
Base saturation voltage	VBE(sat) **	Ic = 1.0 A, Iв = 0.2 A		1.1	1.3	V
Output capacitance	Cob	Vcb = 10 V, IE = 0, f = 1.0 MHz		13	30	pF
Gain bandwidth product	f⊤	Vce = 10 V, Ie = -20 mA	30	60		MHz

\*\* Pulse test PW  $\leq$  350  $\mu$ s, duty cycle  $\leq$  2% per pulsed

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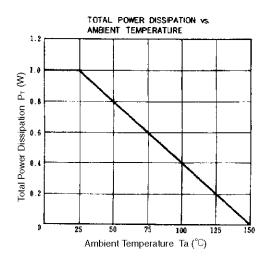
# PACKAGE DRAWING (UNIT: mm)

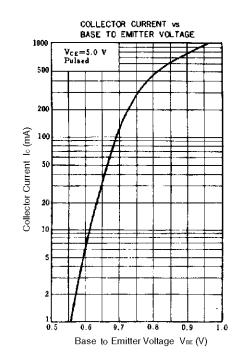


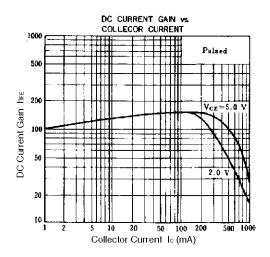
### **hfe CLASSIFICATION**

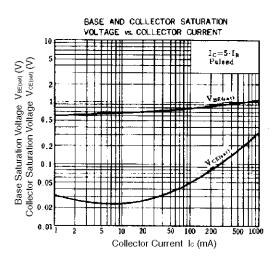
Marking	М	L	К
hfe	100 to 200	160 to 320	200 to 400

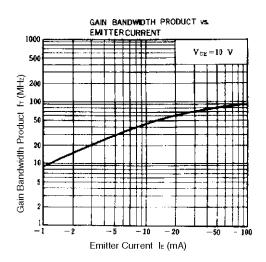
### **TYPICAL CHARACTERISTICS (Ta = 25°C)**

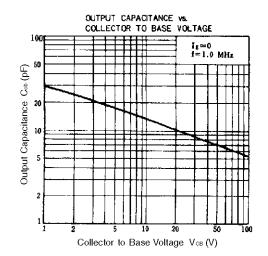












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