

RBN40H125S1FPQ-A0

1250V - 40A - IGBT
Power Switching

R07DS1380EJ0141

Rev.1.41

Oct.14.2021

Features

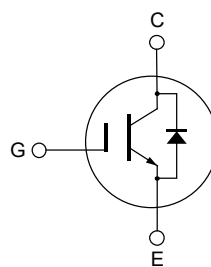
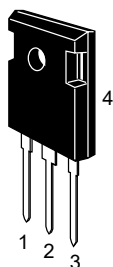
- Trench gate and thin wafer technology (G8H series)
- Built in fast recovery diode in one package
- Low collector to emitter saturation voltage
 $V_{CE(sat)} = 1.8 \text{ V typ. (at } I_C = 40 \text{ A, } V_{GE} = 15 \text{ V, } T_a = 25^\circ\text{C)}$
- Quality grade: Standard
- High speed switching
- Short circuit withstands time (10 $\mu\text{s min.}$)
- Applications: UPS, Welding, photovoltaic inverters, Power converter system

Key Performance

Type	V_{CES}	I_C	$V_{CE(sat)}$, $T_C=25^\circ\text{C}$	I_F	t_{sc}	T_j
RBN40H125S1FPQ-A0	1250 V	40 A	1.8 V	25 A	10 μs	175 $^\circ\text{C}$

Outline

RENESAS Package code: PRSS0003ZH-A
(Package name: TO-247A)



1. Gate
2. Collector
3. Emitter
4. Collector

Absolute Maximum Ratings

(T_c = 25°C)

Item		Symbol	Ratings	Unit
Collector to emitter voltage		V _{CES}	1250	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	T _c = 25 °C	I _C	80	A
	T _c = 100 °C	I _C	40	A
Collector peak current		I _{C(peak)} Notes1	160	A
Diode forward current	T _c = 25 °C	I _F	50	A
	T _c = 100 °C	I _F	25	A
Diode forward peak current		I _{F(peak)} Notes1	160	A
Collector power dissipation		P _C Notes2	319	W
Junction temperature		T _J Notes2	175	°C
Storage temperature		T _{stg}	–55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%

2. Please use this device in the thermal conditions which the junction temperature does not exceed 175 °C.
 Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175 °C.

Thermal Resistance Characteristics

(T_c = 25°C)

Item	Symbol	Max. Value Notes3	Unit
Junction to case thermal resistance (IGBT)	R _{th(j-c)}	0.47	°C/W
Junction to case thermal resistance (Diode)	R _{th(j-c)}	1.61	°C/W

Notes: 3. Designed target value on Renesas measurement condition. (Not tested)

Electrical Characteristics

(Tc = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Collector to emitter leakage current	I_{CES}	—	—	200	μA	$V_{CE} = 1250 V, V_{GE} = 0 V$
Gate to emitter leakage current	I_{GES}	—	—	± 1	μA	$V_{GE} = \pm 30 V, V_{CE} = 0 V$
Gate to emitter threshold voltage	$V_{GE(th)}$	5.3	—	7.1	V	$V_{CE} = 10 V, I_C = 1.3 mA$
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	1.8	2.34	V	$I_C = 40 A, V_{GE} = 15 V$ Notes4
Input capacitance	C_{ies}	—	2330	—	pF	$V_{CE} = 25 V$ $V_{GE} = 0 V$ $f = 1 MHz$
Output capacitance	C_{oes}	—	120	—	pF	
Reverse transfer capacitance	C_{res}	—	18	—	pF	
Total gate charge	Q_g	—	85	—	nC	$V_{GE} = 15 V$ $V_{CE} = 600 V$ $I_C = 40 A$
Gate to emitter charge	Q_{ge}	—	25	—	nC	
Gate to collector charge	Q_{gc}	—	44	—	nC	
Turn-on delay time	$t_{d(on)}$	—	25	—	Ns	$V_{CC} = 600 V$ $V_{GE} = 15 V/-15 V$ $I_C = 40 A$ $R_g = 10 \Omega$ $T_c = 25 ^\circ C$ Inductive load Notes5
Rise time	t_r	—	11	—	ns	
Turn-off delay time	$t_{d(off)}$	—	124	—	ns	
Fall time	t_f	—	67	—	ns	
Turn-on loss energy	E_{on}	—	2.0	—	mJ	
Turn-off loss energy	E_{off}	—	1.4	—	mJ	
Total switching energy	E_{total}	—	3.4	—	mJ	
Turn-on delay time	$t_{d(on)}$	—	26	—	Ns	
Rise time	t_r	—	13	—	ns	
Turn-off delay time	$t_{d(off)}$	—	152	—	ns	
Fall time	t_f	—	106	—	ns	$V_{CC} = 600 V$ $V_{GE} = 15 V/-15 V$ $I_C = 40 A$ $R_g = 10 \Omega$ $T_c = 150 ^\circ C$ Inductive load Notes5
Turn-on loss energy	E_{on}	—	3.3	—	mJ	
Turn-off loss energy	E_{off}	—	2.5	—	mJ	
Total switching energy	E_{total}	—	5.8	—	mJ	
Short circuit withstand time Notes6	t_{sc}	10	—	—	μs	$V_{CC} \leq 720 V, V_{GE} = 15 V$ $T_c \leq 150 ^\circ C$

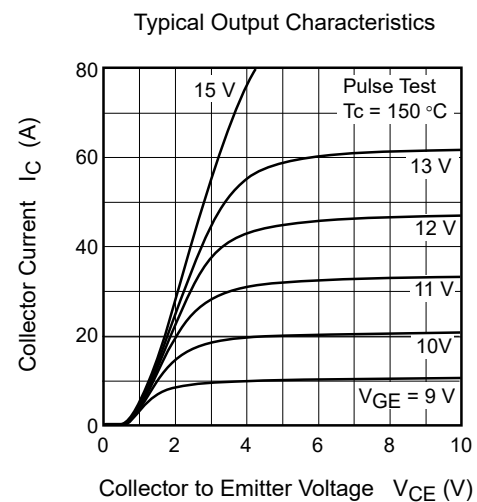
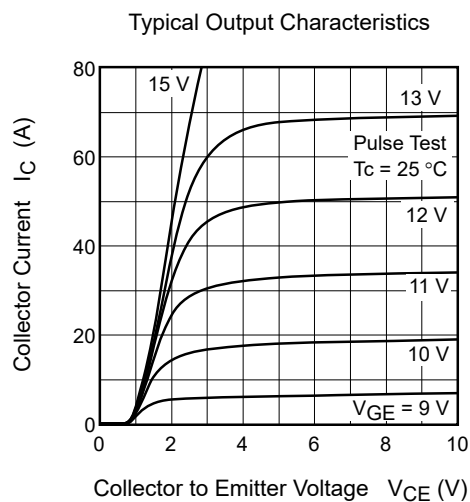
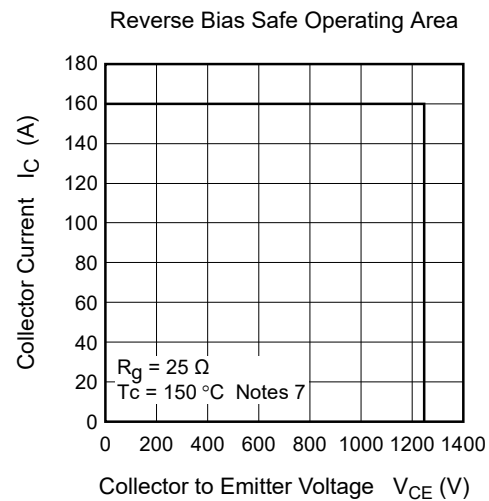
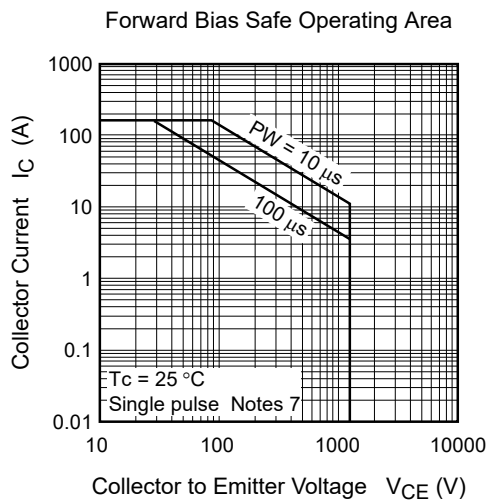
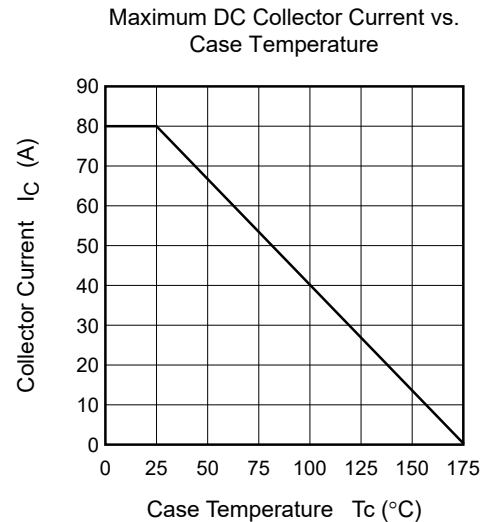
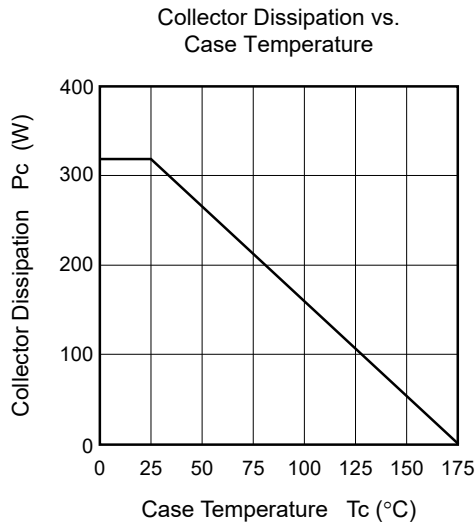
Diode forward voltage	V_F	—	2.8	3.64	V	$I_F = 25 A$ Notes4
Diode reverse recovery time	t_{rr}	—	156	—	ns	$I_F = 25 A, dI_F/dt = 300 A/\mu s$
Diode reverse recovery charge	Q_{rr}	—	0.92	—	μC	
Diode peak reverse recovery current	I_{rr}	—	10	—	A	

Notes: 4. Pulse test

5. Switching time test circuit and waveform are shown below.

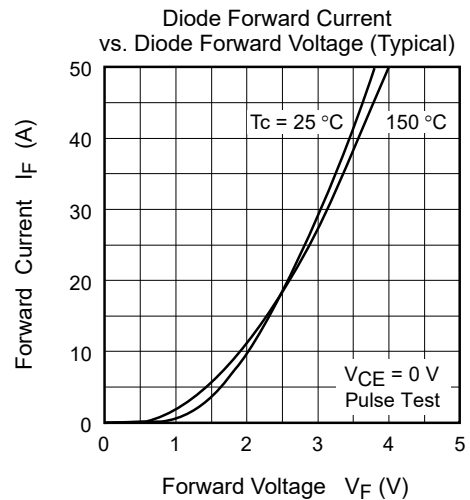
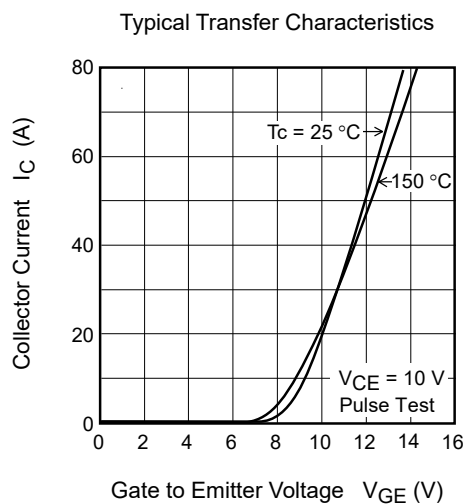
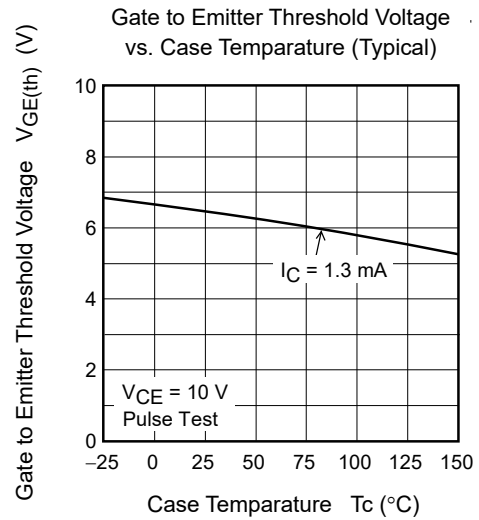
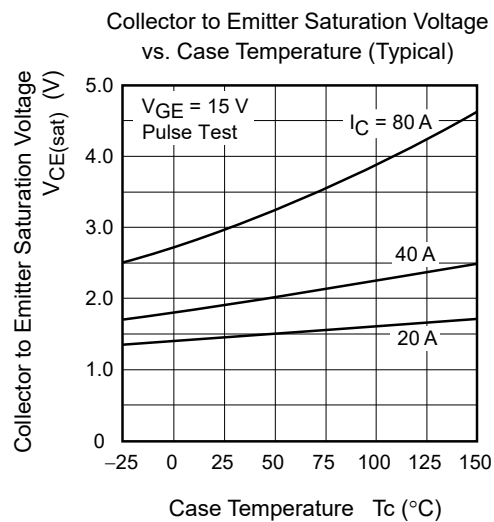
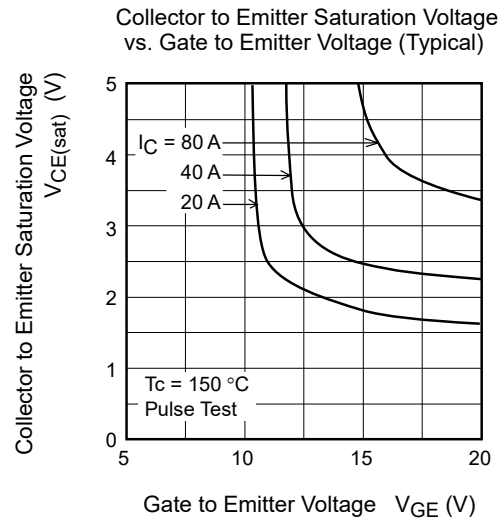
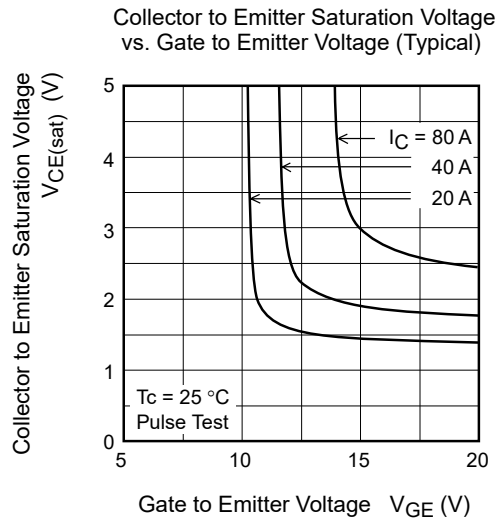
6. Designed target value on Renesas measurement condition. (Not tested)

Main Characteristics

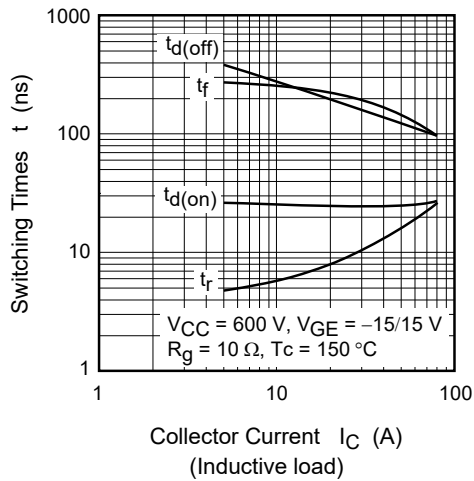


Notes: 7. Designed target value on Renesas measurement condition. (Not tested)

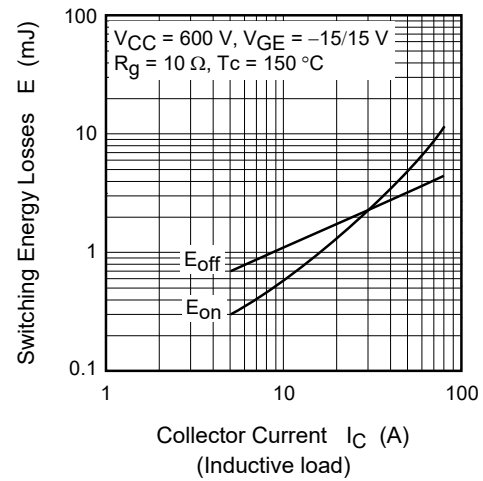
Renesas recommends that operating conditions are designed according to a document "Power MOS FET · IGBT Attention of Handling Semiconductor Devices".



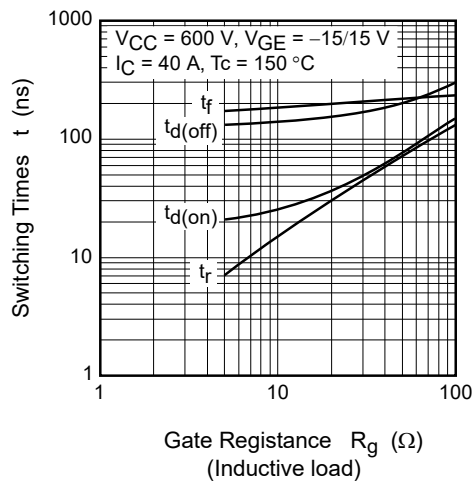
Switching Characteristics (Typical) (1)



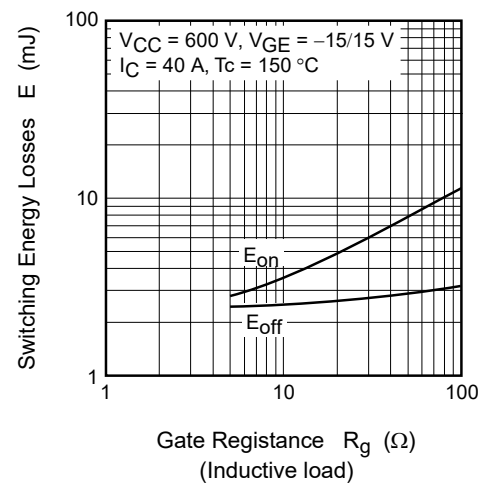
Switching Characteristics (Typical) (2)



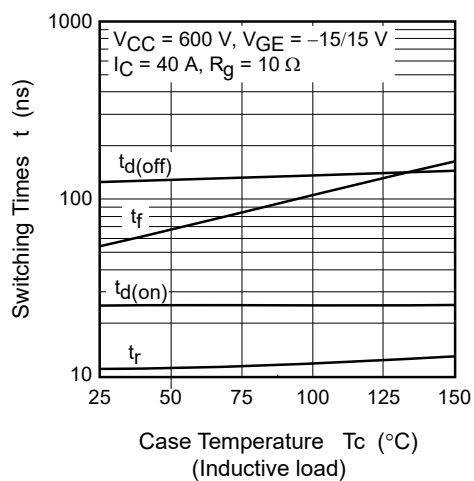
Switching Characteristics (Typical) (3)



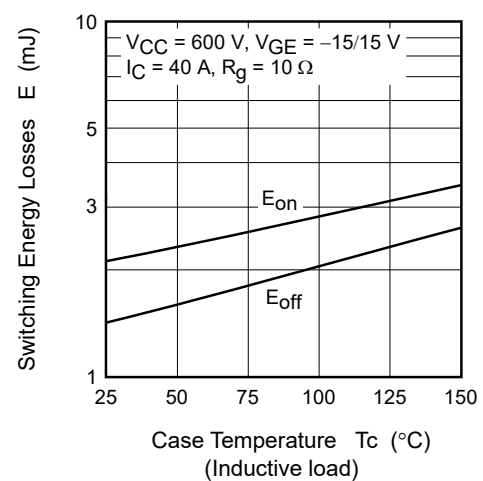
Switching Characteristics (Typical) (4)

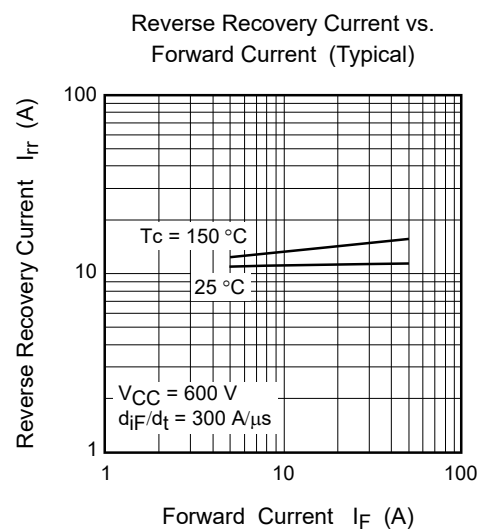
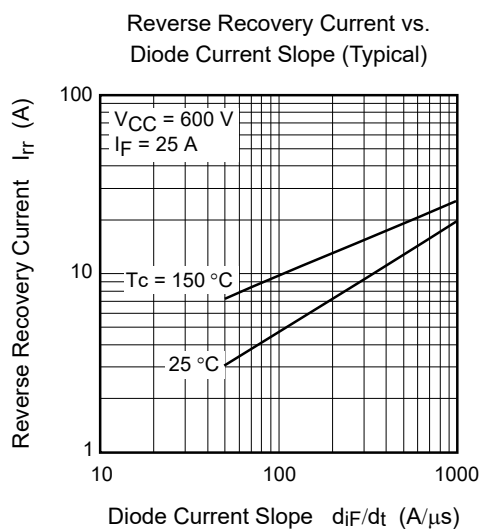
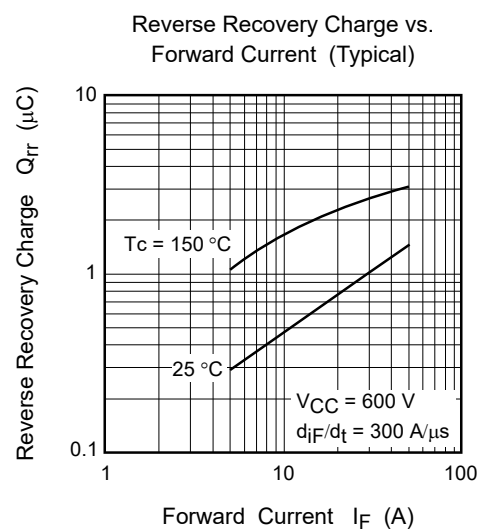
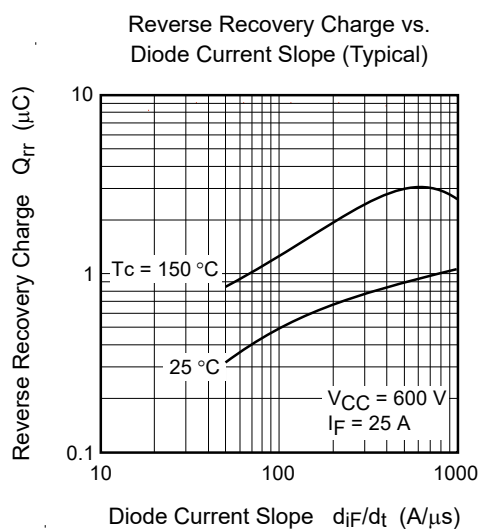
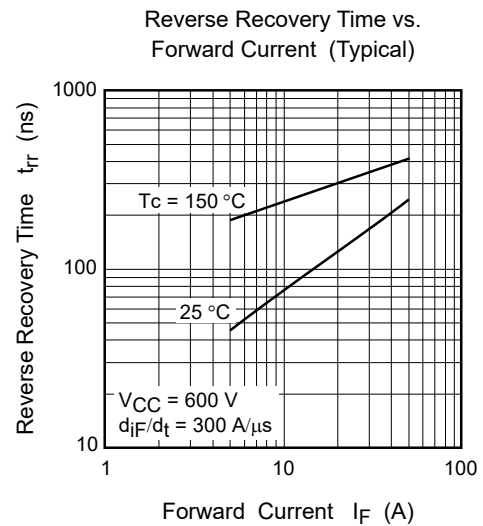
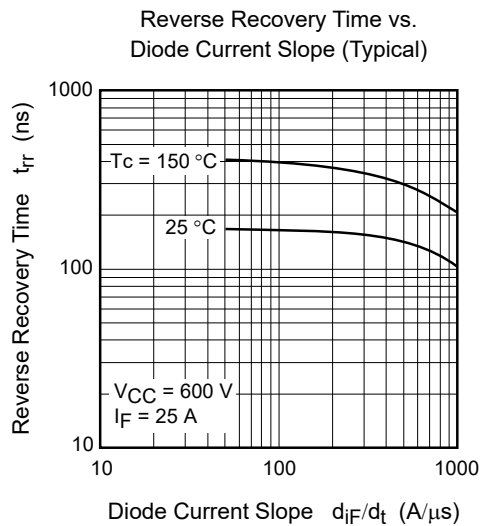


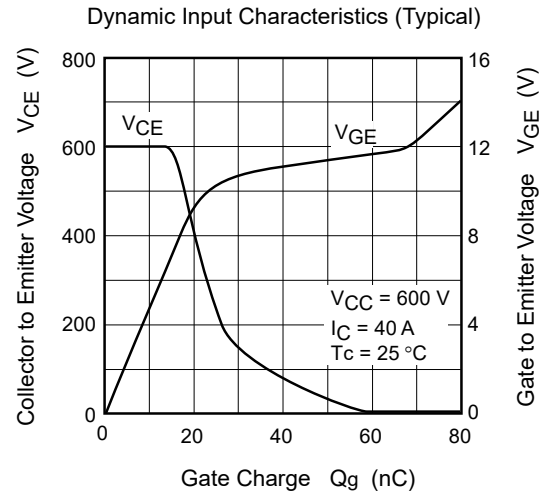
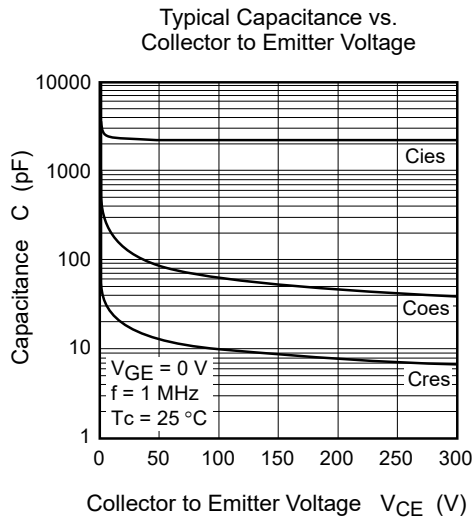
Switching Characteristics (Typical) (5)

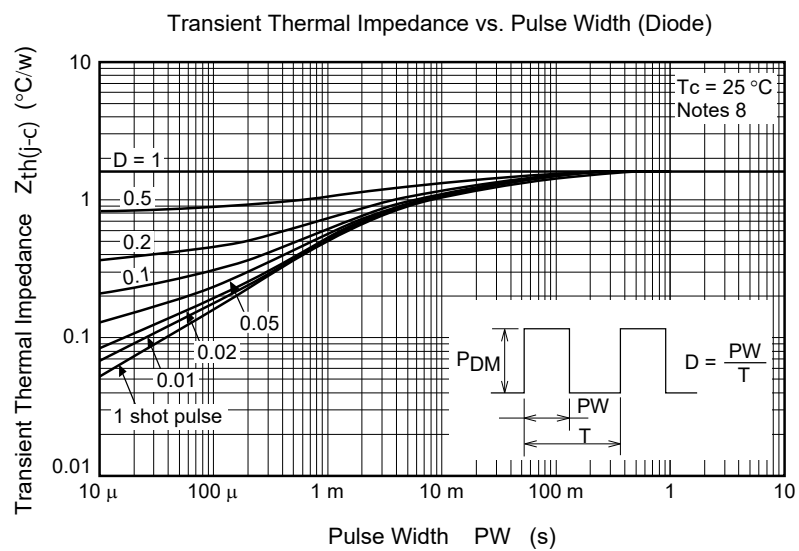
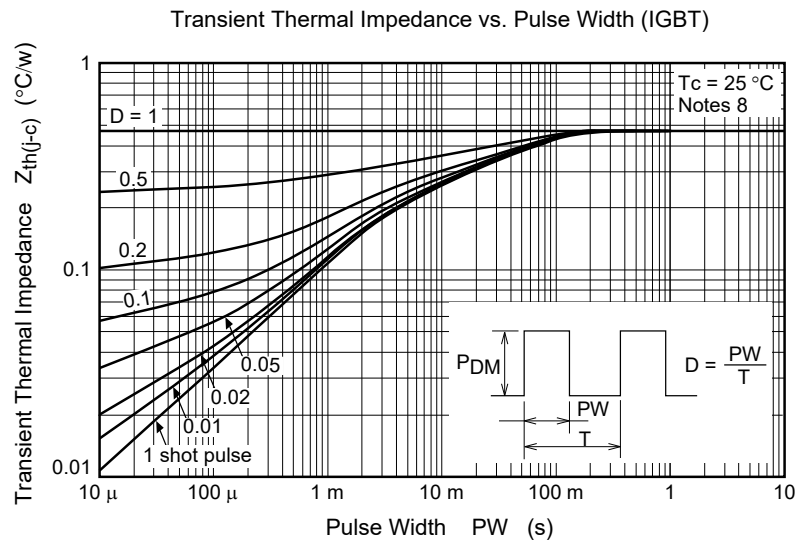


Switching Characteristics (Typical) (6)



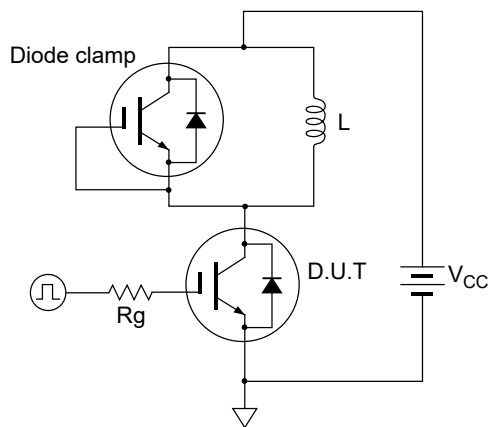




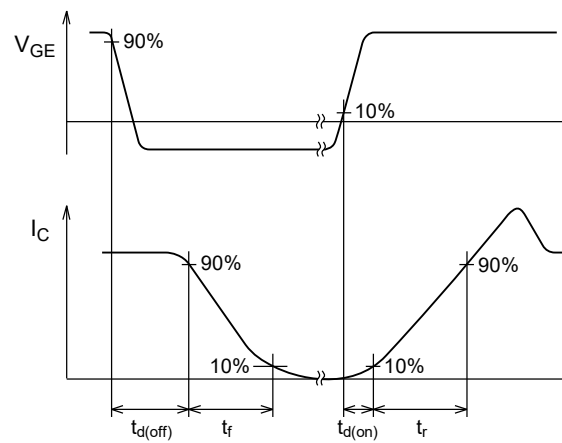


Notes: 8. Designed target value on Renesas measurement condition. (Not tested)

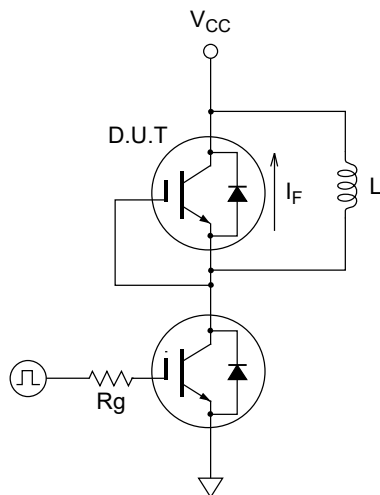
Switching Time Test Circuit



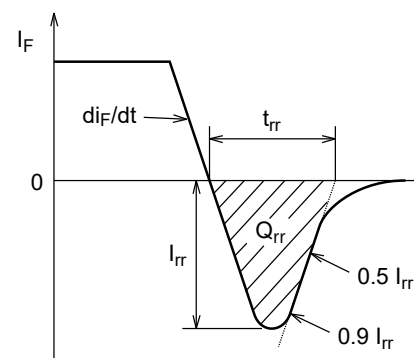
Waveform



Diode Reverse Recovery Time Test Circuit



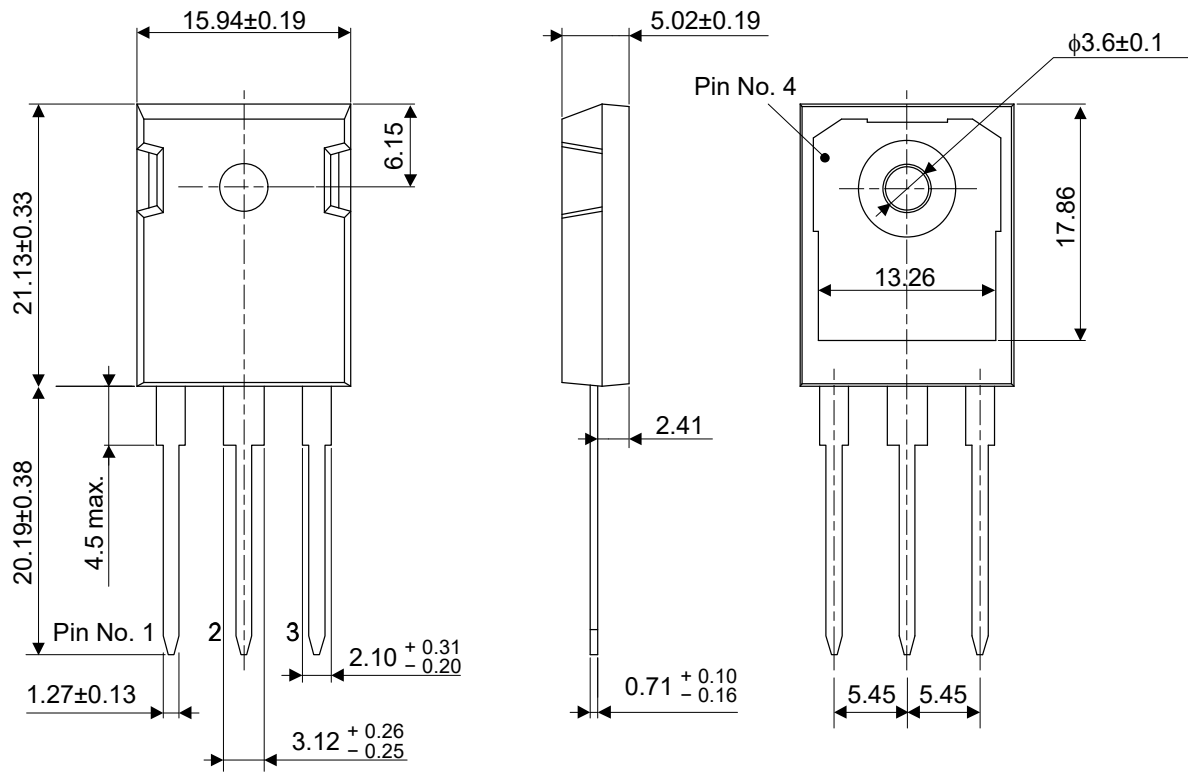
Waveform



Package Dimensions

JEDEC Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
TO-247AD	PRSS0003ZH-A	—	6.14

Unit: mm



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Ordering Information

Orderable Part Number	Quantity	Shipping Container
RBN40H125S1FPQ-A0#CB0	240 pcs	Box (Tube)

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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
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