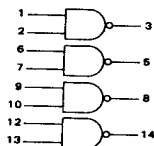
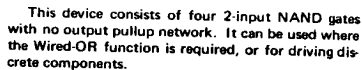


MTTL MC5400F/7400F series

MC5401F*
MC7401F*



Positive Logic: $3 = \overline{1 \cdot 2}$
Negative Logic: $3 = \overline{1 + 2}$

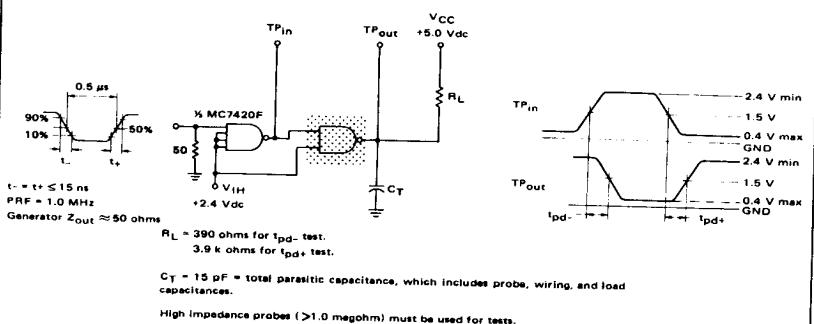
Input Loading Factor = 1
Output Loading Factor = 10

Total Power Dissipation = 40 mW typ/pkg
Propagation Delay Time = 35 ns typ

* F suffix = TO-85 ceramic package (Case 609).
See General Information section for package outline dimensions.

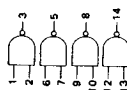
VOLTAGE WAVEFORMS AND DEFINITIONS

SWITCHING TIME TEST CIRCUIT



ELECTRICAL CHARACTERISTICS

Test procedures are shown for only one gate. The other gates are tested in the same manner. Further, test procedures are shown for only one input of the gate under test. To complete testing, sequence through remaining inputs.



TEST CURRENT VOLTAGE VALUES (All Temperatures)									
mA									
Volts									
	V_{OL}	V_{IH}	V_{MHH}	V_{BI}	V_{B2}	V_{B1}	V_{A1}	V_{A2}	V_{CC}
MC5401	16	0.4	2.4	5.5	4.5	5.0	2.0	0.8	5.5
MC7401	16	0.4	2.4	5.5	4.5	5.0	2.0	0.8	5.5
TEST CURRENT VOLTAGE APPLIED TO PINS LISTED BELOW:									
	I_{OL}	V_{OL}	V_{IH}	V_{MHH}	V_{BI}	V_{B2}	V_{B1}	V_{A1}	V_{A2}
Input Forward Current	1	-	-	-	2	-	-	-	-
Leakage Current	1	-	1	-	-	-	-	-	-
Output Voltage	3	-	-	-	-	-	1.2	-	-
Output Leakage Current	3	-	-	-	-	-	-	-	-
Power Requirements (Total Device)	4	-	-	-	-	-	-	-	-
Power Supply Drain	4	-	-	-	-	-	-	-	-
Switching Parameters	4	-	-	-	-	-	-	-	-
Turn-On Delay	1, 3	-	15**	ns	-	-	-	-	-
Turn-Off Delay	1, 3	-	45**	ns	-	-	-	-	-

** Tested only at 25°C.