

# MC74LCX138

## Low-Voltage CMOS 3-to-8 Decoder/Demultiplexer

### With 5 V-Tolerant Inputs

The MC74LCX138 is a high performance, 3-to-8 decoder/demultiplexer operating from a 2.3 to 3.6 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A  $V_I$  specification of 5.5 V allows MC74LCX138 inputs to be safely driven from 5 V devices. The MC74LCX138 is suitable for memory address decoding and other TTL level bus-oriented applications.

The MC74LCX138 high-speed 3-to-8 decoder/demultiplexer accepts three binary weighted inputs (A0, A1, A2) and, when enabled, provides eight mutually exclusive active-LOW outputs ( $\overline{O0}$ – $\overline{O7}$ ). The LCX138 features three Enable inputs, two active-LOW ( $\overline{E1}$ ,  $\overline{E2}$ ) and one active-HIGH (E3). All outputs will be HIGH unless  $\overline{E1}$  and  $\overline{E2}$  are LOW, and E3 is HIGH. This multiple enabled function allows easy parallel expansion of the device to a 1-of-32 (5 lines to 32 lines) decoder with just four LCX138 devices and one inverter (see Figure 1). The LCX138 can be used as an 8-output demultiplexer by using one of the active-LOW Enable inputs as the data input and the other Enable inputs as strobes. The Enable inputs which are not used must be permanently tied to their appropriate active-HIGH or active-LOW state.

Current drive capability is 24 mA at the outputs.

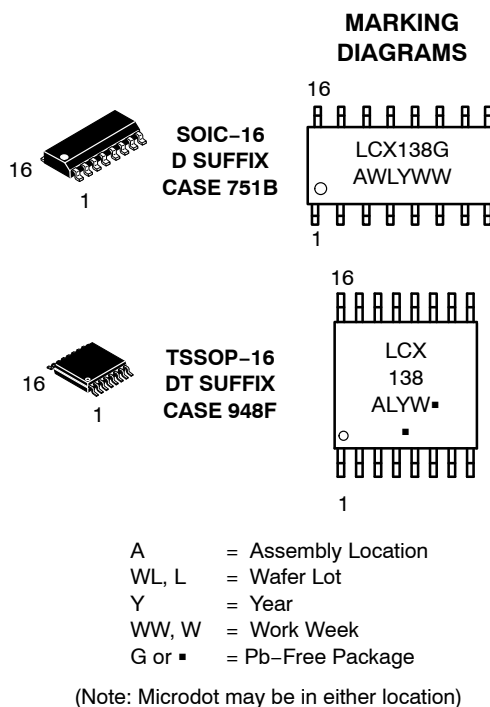
#### Features

- Designed for 2.3 V to 3.6 V  $V_{CC}$  Operation
- 5 V Tolerant Inputs – Interface Capability With 5 V TTL Logic
- LVTTL Compatible
- LVCMOS Compatible
- 24 mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current (10  $\mu$ A) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500 mA
- ESD Performance: Human Body Model >2000 V  
Machine Model >200 V
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant



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#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

# MC74LCX138

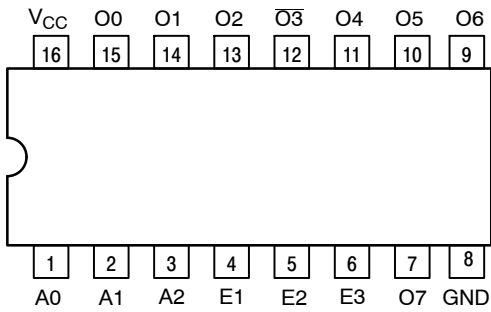


Figure 1. Pinout: 16-Lead (Top View)

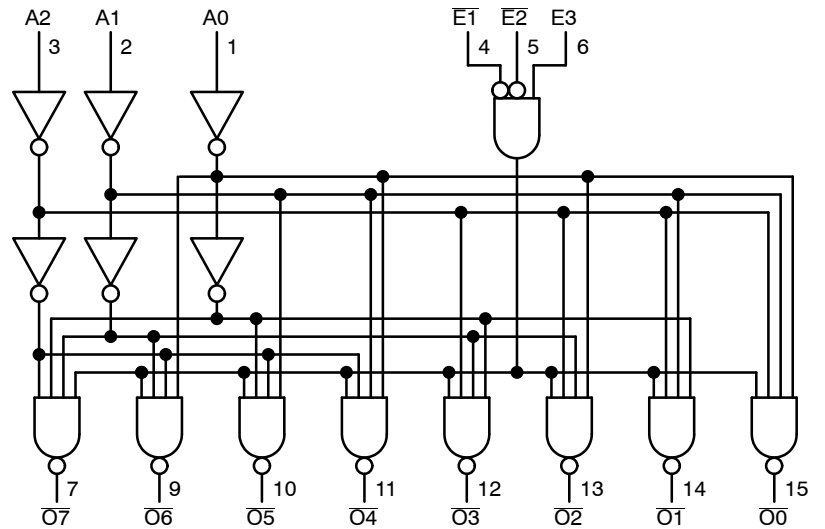


Figure 2. Logic Diagram

## PIN NAMES

| Pins  | Function       |
|-------|----------------|
| A0-A2 | Address Inputs |
| E1-E2 | Enable Inputs  |
| E3    | Enable Input   |
| O0-O7 | Outputs        |

## TRUTH TABLE

| Inputs |    |    |    |    |    | Outputs |    |    |    |    |    |    |    |
|--------|----|----|----|----|----|---------|----|----|----|----|----|----|----|
| E1     | E2 | E3 | A0 | A1 | A2 | O0      | O1 | O2 | O3 | O4 | O5 | O6 | O7 |
| H      | X  | X  | X  | X  | X  | H       | H  | H  | H  | H  | H  | H  | H  |
| X      | H  | X  | X  | X  | X  | H       | H  | H  | H  | H  | H  | H  | H  |
| X      | X  | L  | X  | X  | X  | H       | H  | H  | H  | H  | H  | H  | H  |
| L      | L  | H  | L  | L  | L  | L       | H  | H  | H  | H  | H  | H  | H  |
| L      | L  | H  | H  | L  | L  | H       | L  | H  | H  | H  | H  | H  | H  |
| L      | L  | H  | L  | H  | L  | H       | H  | L  | H  | H  | H  | H  | H  |
| L      | L  | H  | H  | H  | L  | H       | H  | H  | L  | H  | H  | H  | H  |
| L      | L  | H  | L  | L  | H  | H       | H  | H  | H  | L  | H  | H  | H  |
| L      | L  | H  | L  | H  | H  | H       | H  | H  | H  | H  | L  | H  | H  |
| L      | L  | H  | H  | H  | H  | H       | H  | H  | H  | H  | H  | L  | H  |
| L      | L  | H  | H  | H  | H  | H       | H  | H  | H  | H  | H  | H  | L  |

H = High Voltage Level

L = Low Voltage Level

X = High or Low Voltage Level and Transitions are Acceptable

For I<sub>CC</sub> reasons, DO NOT FLOAT Inputs

## MC74LCX138

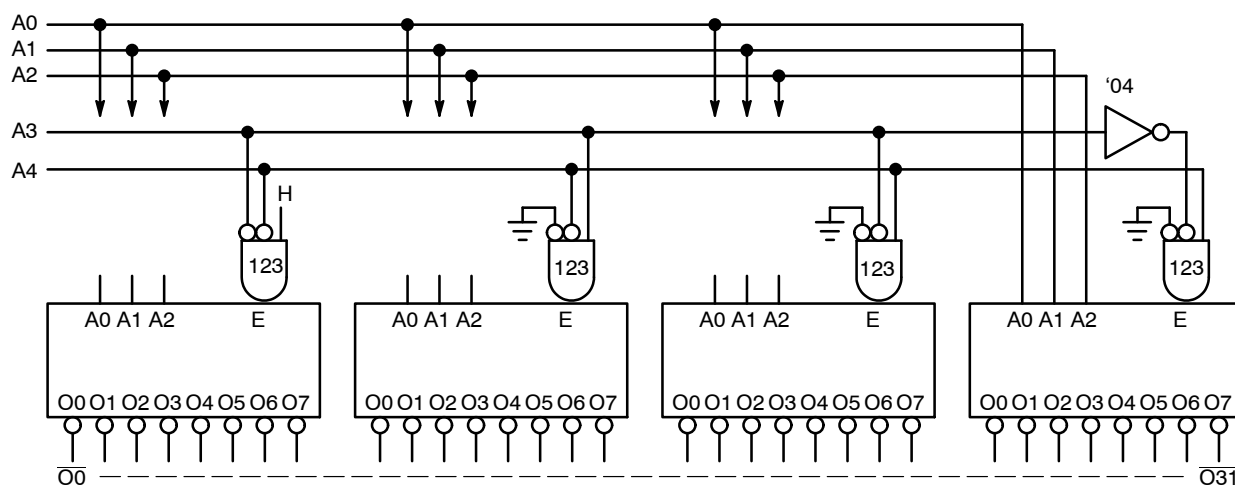


Figure 3. Expansion to 1-of-32 Decoding

### ORDERING INFORMATION

| Device           | Package               | Shipping <sup>†</sup> |
|------------------|-----------------------|-----------------------|
| MC74LCX138DR2G   | SOIC-16<br>(Pb-Free)  | 2500 Tape & Reel      |
| MC74LCX138DTG    | TSSOP-16<br>(Pb-Free) | 96 Units / Rail       |
| MC74LCX138DTR2G  | TSSOP-16<br>(Pb-Free) | 2500 Tape & Reel      |
| NLV74LCX138DR2G* | SOIC-16<br>(Pb-Free)  | 2500 Tape & Reel      |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

### MAXIMUM RATINGS

| Symbol    | Parameter                        | Value                             | Condition                            | Units |
|-----------|----------------------------------|-----------------------------------|--------------------------------------|-------|
| $V_{CC}$  | DC Supply Voltage                | -0.5 to +7.0                      |                                      | V     |
| $V_I$     | DC Input Voltage                 | $-0.5 \leq V_I \leq +7.0$         |                                      | V     |
| $V_O$     | DC Output Voltage                | $-0.5 \leq V_O \leq V_{CC} + 0.5$ | Output in HIGH or LOW State (Note 1) | V     |
| $I_{IK}$  | DC Input Diode Current           | -50                               | $V_I < GND$                          | mA    |
| $I_{OK}$  | DC Output Diode Current          | -50                               | $V_O < GND$                          | mA    |
|           |                                  | +50                               | $V_O > V_{CC}$                       | mA    |
| $I_O$     | DC Output Source/Sink Current    | $\pm 50$                          |                                      | mA    |
| $I_{CC}$  | DC Supply Current Per Supply Pin | $\pm 100$                         |                                      | mA    |
| $I_{GND}$ | DC Ground Current Per Ground Pin | $\pm 100$                         |                                      | mA    |
| $T_{STG}$ | Storage Temperature Range        | -65 to +150                       |                                      | °C    |
| MSL       | Moisture Sensitivity             |                                   | Level 1                              |       |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1.  $I_O$  absolute maximum rating must be observed.

# MC74LCX138

## RECOMMENDED OPERATING CONDITIONS

| Symbol          | Parameter                                                                                                                          | Min        | Typ                  | Max              | Units |
|-----------------|------------------------------------------------------------------------------------------------------------------------------------|------------|----------------------|------------------|-------|
| V <sub>CC</sub> | Supply Voltage<br>Operating Data Retention Only                                                                                    | 2.0<br>1.5 | 2.5, 3.3<br>2.5, 3.3 | 3.6<br>3.6       | V     |
| V <sub>I</sub>  | Input Voltage                                                                                                                      | 0          |                      | 5.5              | V     |
| V <sub>O</sub>  | Output Voltage<br>(HIGH or LOW State) (3-State)                                                                                    | 0          |                      | V <sub>CC</sub>  | V     |
| I <sub>OH</sub> | HIGH Level Output Current<br>V <sub>CC</sub> = 3.0 V – 3.6 V<br>V <sub>CC</sub> = 2.7 V – 3.0 V<br>V <sub>CC</sub> = 2.3 V – 2.7 V |            |                      | –24<br>–12<br>–8 | mA    |
| I <sub>OL</sub> | LOW Level Output Current<br>V <sub>CC</sub> = 3.0 V – 3.6 V<br>V <sub>CC</sub> = 2.7 V – 3.0 V<br>V <sub>CC</sub> = 2.3 V – 2.7 V  |            |                      | +24<br>+12<br>+8 | mA    |
| T <sub>A</sub>  | Operating Free-Air Temperature                                                                                                     | –40        |                      | +85              | °C    |
| Δt/ΔV           | Input Transition Rise or Fall Rate, V <sub>IN</sub> from 0.8 V to 2.0 V, V <sub>CC</sub> = 3.0 V                                   | 0          |                      | 10               | ns/V  |

## DC ELECTRICAL CHARACTERISTICS

| Symbol           | Characteristic                        | Condition                                                                | T <sub>A</sub> = –40°C to +85°C |      | Units |
|------------------|---------------------------------------|--------------------------------------------------------------------------|---------------------------------|------|-------|
|                  |                                       |                                                                          | Min                             | Max  |       |
| V <sub>IH</sub>  | HIGH Level Input Voltage (Note 2)     | 2.3 V ≤ V <sub>CC</sub> ≤ 2.7 V                                          | 1.7                             |      | V     |
|                  |                                       | 2.7 V ≤ V <sub>CC</sub> ≤ 3.6 V                                          | 2.0                             |      |       |
| V <sub>IL</sub>  | LOW Level Input Voltage (Note 2)      | 2.3 V ≤ V <sub>CC</sub> ≤ 2.7 V                                          |                                 | 0.7  | V     |
|                  |                                       | 2.7 V ≤ V <sub>CC</sub> ≤ 3.6 V                                          |                                 | 0.8  |       |
| V <sub>OH</sub>  | HIGH Level Output Voltage             | 2.3 V ≤ V <sub>CC</sub> ≤ 3.6 V; I <sub>OH</sub> = –100 μA               | V <sub>CC</sub> – 0.2           |      | V     |
|                  |                                       | V <sub>CC</sub> = 2.3 V; I <sub>OH</sub> = –8 mA                         | 1.8                             |      |       |
|                  |                                       | V <sub>CC</sub> = 2.7 V; I <sub>OH</sub> = –12 mA                        | 2.2                             |      |       |
|                  |                                       | V <sub>CC</sub> = 3.0 V; I <sub>OH</sub> = –18 mA                        | 2.4                             |      |       |
|                  |                                       | V <sub>CC</sub> = 3.0 V; I <sub>OH</sub> = –24 mA                        | 2.2                             |      |       |
| V <sub>OL</sub>  | LOW Level Output Voltage              | 2.3 V ≤ V <sub>CC</sub> ≤ 3.6 V; I <sub>OL</sub> = 100 μA                |                                 | 0.2  | V     |
|                  |                                       | V <sub>CC</sub> = 2.3 V; I <sub>OL</sub> = 8 mA                          |                                 | 0.6  |       |
|                  |                                       | V <sub>CC</sub> = 2.7 V; I <sub>OL</sub> = 12 mA                         |                                 | 0.4  |       |
|                  |                                       | V <sub>CC</sub> = 3.0 V; I <sub>OL</sub> = 16 mA                         |                                 | 0.4  |       |
|                  |                                       | V <sub>CC</sub> = 3.0 V; I <sub>OL</sub> = 24 mA                         |                                 | 0.55 |       |
| I <sub>OFF</sub> | Power Off Leakage Current             | V <sub>CC</sub> = 0, V <sub>IN</sub> = 5.5 V or V <sub>OUT</sub> = 5.5 V |                                 | 10   | μA    |
| I <sub>IN</sub>  | Input Leakage Current                 | V <sub>CC</sub> = 3.6 V, V <sub>IN</sub> = 5.5 V or GND                  |                                 | ±5   | μA    |
| I <sub>CC</sub>  | Quiescent Supply Current              | V <sub>CC</sub> = 3.6 V, V <sub>IN</sub> = 5.5 V or GND                  |                                 | 10   | μA    |
| ΔI <sub>CC</sub> | Increase in I <sub>CC</sub> per Input | 2.3 ≤ V <sub>CC</sub> ≤ 3.6 V; V <sub>IH</sub> = V <sub>CC</sub> – 0.6 V |                                 | 500  | μA    |

2. These values of V<sub>I</sub> are used to test DC electrical characteristics only.

## AC CHARACTERISTICS (t<sub>R</sub> = t<sub>F</sub> = 2.5 ns; R<sub>L</sub> = 500 Ω)

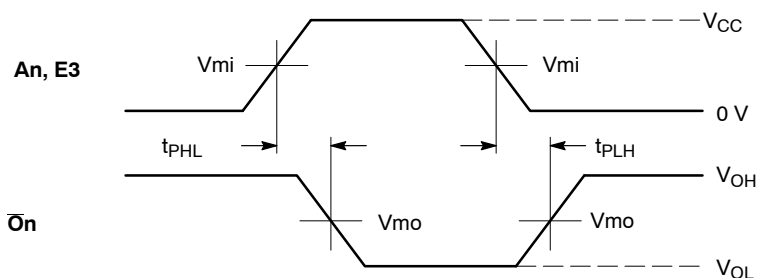
| Symbol                                 | Parameter                         | Waveform | Limits                          |            |                         |            |                                 |            | Units |
|----------------------------------------|-----------------------------------|----------|---------------------------------|------------|-------------------------|------------|---------------------------------|------------|-------|
|                                        |                                   |          | T <sub>A</sub> = –40°C to +85°C |            |                         |            |                                 |            |       |
|                                        |                                   |          | V <sub>CC</sub> = 3.3 V ± 0.3 V |            | V <sub>CC</sub> = 2.7 V |            | V <sub>CC</sub> = 2.5 V ± 0.2 V |            |       |
|                                        |                                   |          | C <sub>L</sub> = 50 pF          |            | C <sub>L</sub> = 50 pF  |            | C <sub>L</sub> = 30 pF          |            |       |
|                                        |                                   |          | Min                             | Max        | Min                     | Max        | Min                             | Max        |       |
| t <sub>PLH</sub><br>t <sub>PHL</sub>   | Propagation Delay<br>An to On     | 1, 2     | 1.5<br>1.5                      | 6.0<br>6.0 | 1.5<br>1.5              | 7.0<br>7.0 | 1.5<br>1.5                      | 7.2<br>7.2 | ns    |
| t <sub>PLH</sub><br>t <sub>PHL</sub>   | Propagation Delay<br>E1, E2 to On | 2        | 1.5<br>1.5                      | 6.5<br>6.5 | 1.5<br>1.5              | 7.5<br>7.5 | 1.5<br>1.5                      | 8.4<br>8.4 | ns    |
| t <sub>PLH</sub><br>t <sub>PHL</sub>   | Propagation Delay<br>E3 to On     | 1        | 1.5<br>1.5                      | 6.0<br>6.0 | 1.5<br>1.5              | 7.0<br>7.0 | 1.5<br>1.5                      | 7.2<br>7.2 | ns    |
| t <sub>OSHL</sub><br>t <sub>OSLH</sub> | Output–to–Output Skew<br>(Note 3) |          |                                 | 1.0<br>1.0 |                         |            |                                 |            | ns    |

3. Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH-to-LOW (t<sub>OSHL</sub>) or LOW-to-HIGH (t<sub>OSLH</sub>); parameter guaranteed by design.

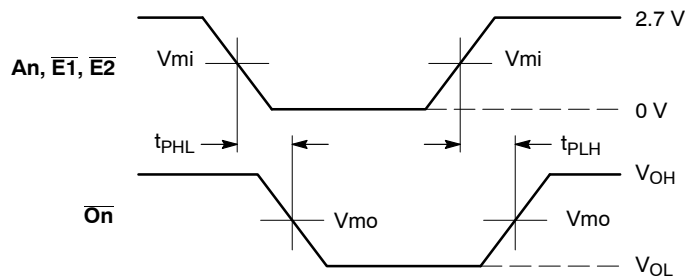
# MC74LCX138

## CAPACITIVE CHARACTERISTICS

| Symbol    | Parameter                     | Condition                                                          | Typical | Units |
|-----------|-------------------------------|--------------------------------------------------------------------|---------|-------|
| $C_{IN}$  | Input Capacitance             | $V_{CC} = 3.3 \text{ V}$ , $V_I = 0 \text{ V}$ or $V_{CC}$         | 7       | pF    |
| $C_{OUT}$ | Output Capacitance            | $V_{CC} = 3.3 \text{ V}$ , $V_I = 0 \text{ V}$ or $V_{CC}$         | 8       | pF    |
| $C_{PD}$  | Power Dissipation Capacitance | 10 MHz, $V_{CC} = 3.3 \text{ V}$ , $V_I = 0 \text{ V}$ or $V_{CC}$ | 25      | pF    |



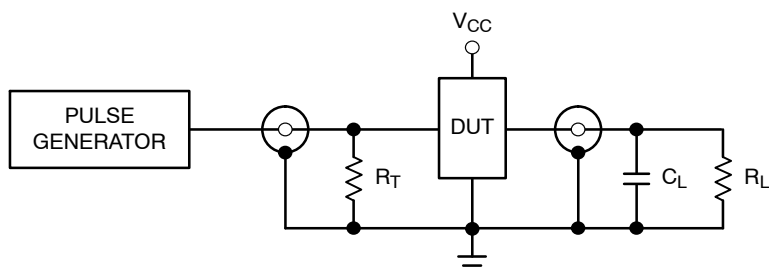
WAVEFORM 1: PROPAGATION DELAYS FOR INVERTING OUTPUTS



WAVEFORM 2: PROPAGATION DELAYS FOR NON-INVERTING OUTPUTS

| Symbol   | $V_{CC}$                          |                 |                                   |
|----------|-----------------------------------|-----------------|-----------------------------------|
|          | $3.3 \text{ V} \pm 0.3 \text{ V}$ | $2.7 \text{ V}$ | $2.5 \text{ V} \pm 0.2 \text{ V}$ |
| $V_{mi}$ | 1.5 V                             | 1.5 V           | $V_{CC}/2$                        |
| $V_{mo}$ | 1.5 V                             | 1.5 V           | $V_{CC}/2$                        |

Figure 4. AC Waveforms



$C_L = 50 \text{ pF}$  at  $V_{CC} = 3.3 \pm 0.3 \text{ V}$  or equivalent (includes jig and probe capacitance)  
 $C_L = 30 \text{ pF}$  at  $V_{CC} = 2.5 \pm 0.2 \text{ V}$  or equivalent (includes jig and probe capacitance)  
 $R_L = R_1 = 500 \Omega$  or equivalent  
 $R_T = Z_{OUT}$  of pulse generator (typically  $50 \Omega$ )

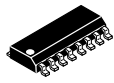
Figure 5. Test Circuit

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

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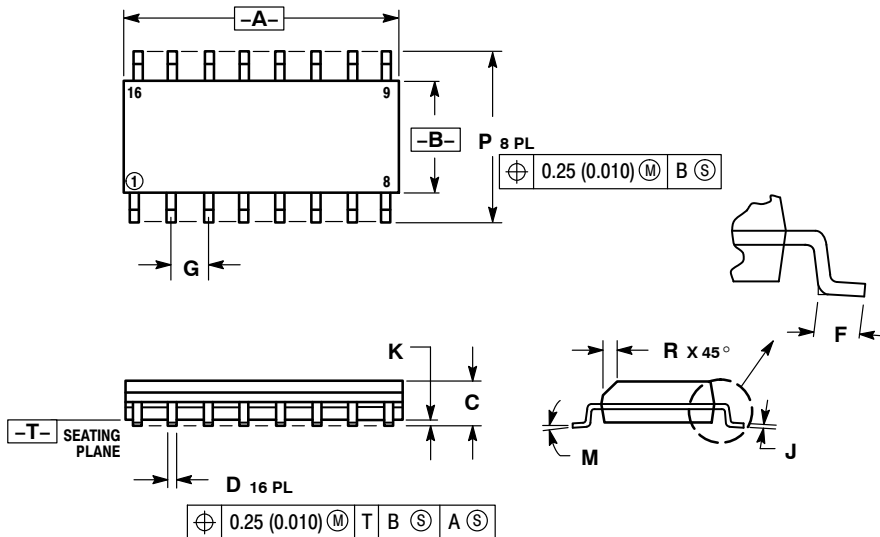
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### SOIC-16 CASE 751B-05 ISSUE K

DATE 29 DEC 2006



#### NOTES:

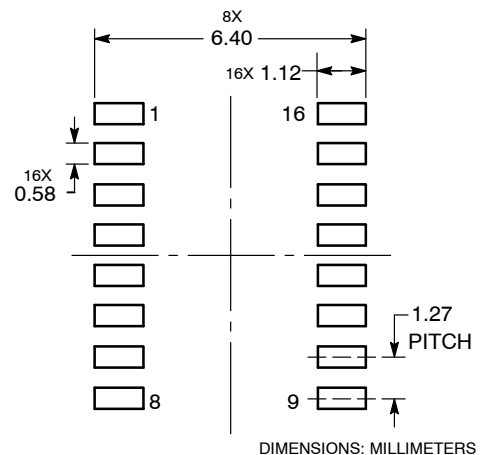
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

| DIM | MILLIMETERS |       | INCHES    |       |
|-----|-------------|-------|-----------|-------|
|     | MIN         | MAX   | MIN       | MAX   |
| A   | 9.80        | 10.00 | 0.386     | 0.393 |
| B   | 3.80        | 4.00  | 0.150     | 0.157 |
| C   | 1.35        | 1.75  | 0.054     | 0.068 |
| D   | 0.35        | 0.49  | 0.014     | 0.019 |
| F   | 0.40        | 1.25  | 0.016     | 0.049 |
| G   | 1.27 BSC    |       | 0.050 BSC |       |
| J   | 0.19        | 0.25  | 0.008     | 0.009 |
| K   | 0.10        | 0.25  | 0.004     | 0.009 |
| M   | 0°          | 7°    | 0°        | 7°    |
| P   | 5.80        | 6.20  | 0.229     | 0.244 |
| R   | 0.25        | 0.50  | 0.010     | 0.019 |

|                   |                   |                          |                          |
|-------------------|-------------------|--------------------------|--------------------------|
| STYLE 1:          | STYLE 2:          | STYLE 3:                 | STYLE 4:                 |
| PIN 1. COLLECTOR  | PIN 1. CATHODE    | PIN 1. COLLECTOR, DYE #1 | PIN 1. COLLECTOR, DYE #1 |
| 2. BASE           | 2. ANODE          | 2. BASE, #1              | 2. COLLECTOR, #1         |
| 3. EMITTER        | 3. NO CONNECTION  | 3. EMITTER, #1           | 3. COLLECTOR, #2         |
| 4. NO CONNECTION  | 4. CATHODE        | 4. COLLECTOR, #1         | 4. COLLECTOR, #2         |
| 5. EMITTER        | 5. CATHODE        | 5. COLLECTOR, #2         | 5. COLLECTOR, #3         |
| 6. BASE           | 6. NO CONNECTION  | 6. BASE, #2              | 6. COLLECTOR, #3         |
| 7. COLLECTOR      | 7. ANODE          | 7. EMITTER, #2           | 7. COLLECTOR, #4         |
| 8. COLLECTOR      | 8. CATHODE        | 8. COLLECTOR, #2         | 8. COLLECTOR, #4         |
| 9. BASE           | 9. CATHODE        | 9. COLLECTOR, #3         | 9. BASE, #4              |
| 10. EMITTER       | 10. ANODE         | 10. BASE, #3             | 10. EMITTER, #4          |
| 11. NO CONNECTION | 11. NO CONNECTION | 11. EMITTER, #3          | 11. BASE, #3             |
| 12. EMITTER       | 12. CATHODE       | 12. COLLECTOR, #3        | 12. EMITTER, #3          |
| 13. BASE          | 13. CATHODE       | 13. COLLECTOR, #4        | 13. BASE, #2             |
| 14. COLLECTOR     | 14. NO CONNECTION | 14. BASE, #4             | 14. EMITTER, #2          |
| 15. EMITTER       | 15. ANODE         | 15. EMITTER, #4          | 15. BASE, #1             |
| 16. COLLECTOR     | 16. CATHODE       | 16. COLLECTOR, #4        | 16. EMITTER, #1          |

|                      |                |                           |
|----------------------|----------------|---------------------------|
| STYLE 5:             | STYLE 6:       | STYLE 7:                  |
| PIN 1. DRAIN, DYE #1 | PIN 1. CATHODE | PIN 1. SOURCE N-CH        |
| 2. DRAIN, #1         | 2. CATHODE     | 2. COMMON DRAIN (OUTPUT)  |
| 3. DRAIN, #2         | 3. CATHODE     | 3. COMMON DRAIN (OUTPUT)  |
| 4. DRAIN, #2         | 4. CATHODE     | 4. GATE P-CH              |
| 5. DRAIN, #3         | 5. CATHODE     | 5. COMMON DRAIN (OUTPUT)  |
| 6. DRAIN, #3         | 6. CATHODE     | 6. COMMON DRAIN (OUTPUT)  |
| 7. DRAIN, #4         | 7. CATHODE     | 7. COMMON DRAIN (OUTPUT)  |
| 8. DRAIN, #4         | 8. CATHODE     | 8. SOURCE P-CH            |
| 9. GATE, #4          | 9. ANODE       | 9. SOURCE P-CH            |
| 10. SOURCE, #4       | 10. ANODE      | 10. COMMON DRAIN (OUTPUT) |
| 11. GATE, #3         | 11. ANODE      | 11. COMMON DRAIN (OUTPUT) |
| 12. SOURCE, #3       | 12. ANODE      | 12. COMMON DRAIN (OUTPUT) |
| 13. GATE, #2         | 13. ANODE      | 13. GATE N-CH             |
| 14. SOURCE, #2       | 14. ANODE      | 14. COMMON DRAIN (OUTPUT) |
| 15. GATE, #1         | 15. ANODE      | 15. COMMON DRAIN (OUTPUT) |
| 16. SOURCE, #1       | 16. ANODE      | 16. SOURCE N-CH           |

#### SOLDERING FOOTPRINT



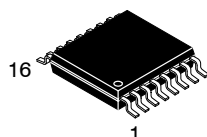
DIMENSIONS: MILLIMETERS

|                  |             |                                                                                                                                                                                     |
|------------------|-------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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| DESCRIPTION:     | SOIC-16     | PAGE 1 OF 1                                                                                                                                                                         |

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# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

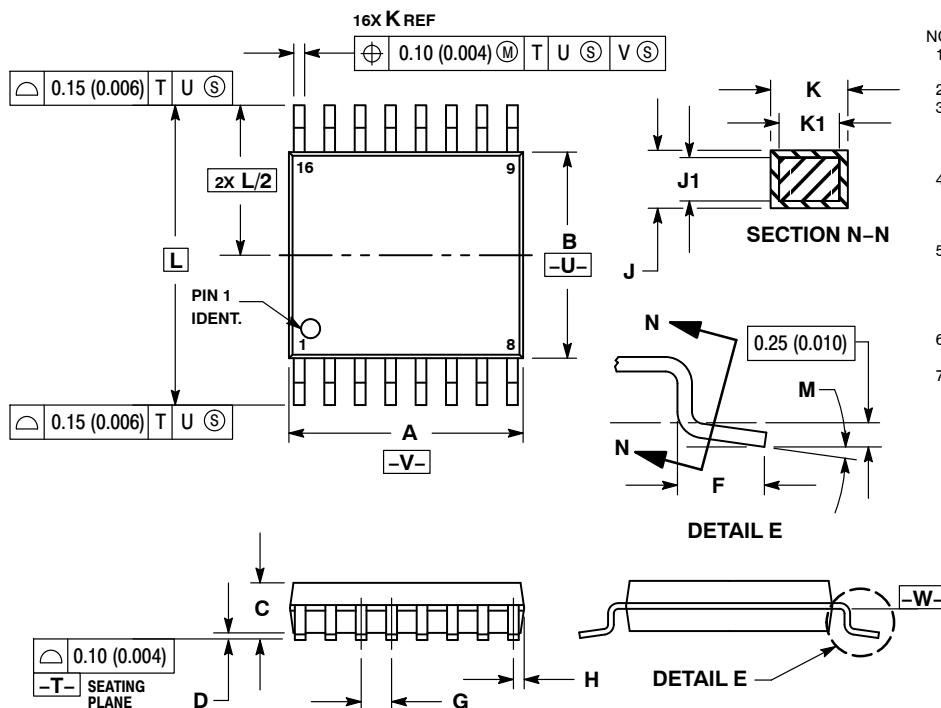
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SCALE 2:1

## TSSOP-16 CASE 948F-01 ISSUE B

DATE 19 OCT 2006

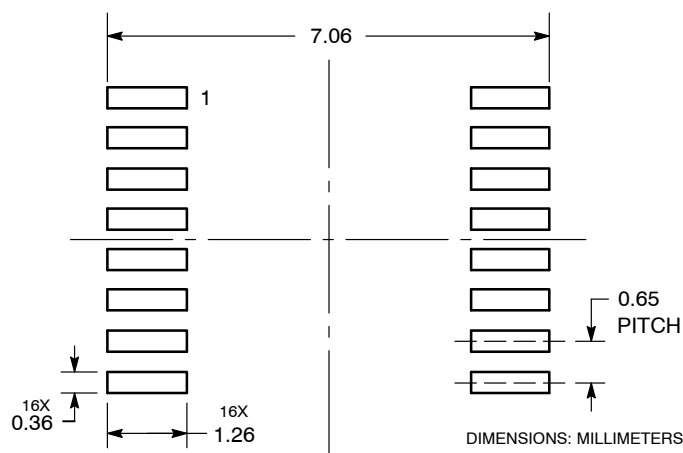


### NOTES:

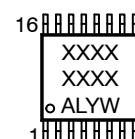
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH. PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE -W-.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 4.90        | 5.10 | 0.193     | 0.200 |
| B   | 4.30        | 4.50 | 0.169     | 0.177 |
| C   | ---         | 1.20 | ---       | 0.047 |
| D   | 0.05        | 0.15 | 0.002     | 0.006 |
| F   | 0.50        | 0.75 | 0.020     | 0.030 |
| G   | 0.65 BSC    |      | 0.026 BSC |       |
| H   | 0.18        | 0.28 | 0.007     | 0.011 |
| J   | 0.09        | 0.20 | 0.004     | 0.008 |
| J1  | 0.09        | 0.16 | 0.004     | 0.006 |
| K   | 0.19        | 0.30 | 0.007     | 0.012 |
| K1  | 0.19        | 0.25 | 0.007     | 0.010 |
| L   | 6.40 BSC    |      | 0.252 BSC |       |
| M   | 0°          | 8°   | 0°        | 8°    |

### SOLDERING FOOTPRINT



### GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code  
A = Assembly Location  
L = Wafer Lot  
Y = Year  
W = Work Week  
G or ■ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present.

|                  |             |                                                                                                                                                                                     |
|------------------|-------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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| DESCRIPTION:     | TSSOP-16    | PAGE 1 OF 1                                                                                                                                                                         |

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