

# AP0100AT

## Image Signal Processor, 1 MP

### Product Overview

For complete documentation, see the data sheet.

AP0100AT is a dedicated automotive image co-processor that enables flexible camera platforms using high performance ON Semiconductor megapixel high dynamic range (HDR) sensors. The two-chip solution of sensor and co-processor allows for multiple camera price and performance points with re-use of circuit board design, fast time to market and design flexibility. Sensor performance is enhanced by the reduction in heat resulting from a separate co-processor chip while at the same time enabling high performance features. The AP0100AT supports 185-degree fisheye lens distortion correction, perspective correction and multiple view options such as split side view, triptych and trailer hitch. This co-processor works with ON Semiconductor's AR0132AT and AR0140AT HDR automotive sensors, and is ideal for NTSC/PAL backup cameras.

### Features

- High Dynamic Range

### Applications

- Automotive

| Part Electrical Specifications |                   |            |        |                  |                           |               |                    |              |
|--------------------------------|-------------------|------------|--------|------------------|---------------------------|---------------|--------------------|--------------|
| Product                        | Pricing (\$/Unit) | Compliance | Status | Frame Rate (fps) | Video                     | Output Format | Compatible Sensors | Package Type |
| AP0100AT2L00<br>XUGA0-DR       |                   |            | Active | 45               | 720p/60 fps ,<br>NTSC/PAL | YUV           |                    | VFPGA-100    |
| AP0100AT2L00<br>XUGA0-DR1      |                   |            | Active | 45               | 720p/60 fps ,<br>NTSC/PAL | YUV           |                    | VFPGA-100    |
| AP0100AT2L00<br>XUGA0-TR       |                   |            | Active | 45               | 720p/60 fps ,<br>NTSC/PAL | YUV           |                    | VFPGA-100    |