

## **AP0100CS**

# Image Signal Processor, 1 MP

### **Product Overview**

For complete documentation, see the data sheet.

AP0100CS is a dedicated image co-processor that enables flexible camera platforms using high performance ON Semiconductor megapixel high dynamic range (HDR) sensors. The two-chip solution of sensor and co-processor allows for multiple camera price and performance points with re-use of circuit board design, fast time to market and design flexibility. Sensor performance is enhanced by the reduction in heat resulting from a separate co-processor chip while at the same time enabling high performance features. The AP0100CS supports 185-degree fisheye lens distortion correction, perspective correction and multiple view options such as split side view, triptych and trailer hitch.

#### **Features**

· High Dynamic Range

#### **Applications**

Security

Part Electrical Specifications								
Product	Pricing (\$/Unit)	Complian ce	Status	Frame Rate (fps)	Video	Output Format	Compatible Sensors	Package Type
AP0100CS2L00 SUGA0-DR		Pb H	Active	45				VFBGA-100